

# ECE 3040 Microelectronic Circuits

*Exam 3*

*April 17, 2003*

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Print your name clearly and largely:

*Solutions*

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## **Instructions:**

Read all the problems carefully and thoroughly before you begin working. You are allowed to use 1 new sheet of notes (1 page front and back), your note sheet from the previous exams as well as a calculator. There are 105 total points in this exam (5 bonus points). The exam will be graded on a 100-point basis. Observe the point value of each problem and allocate your time accordingly. **SHOW ALL WORK AND CIRCLE YOUR FINAL ANSWER WITH THE PROPER UNITS INDICATED.** Write legibly. If I cannot read it, it will be considered a wrong answer. Do all work on the paper provided. Turn in all scratch paper, even if it did not lead to an answer. Report any and all ethics violations to the instructor. Good luck!

Sign your name on ONE of the two following cases:

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I DID NOT observe any ethical violations during this exam:

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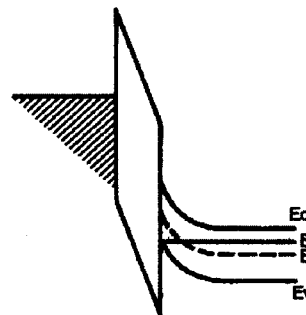
I observed an ethical violation during this exam:



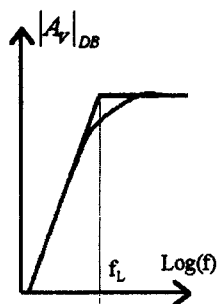
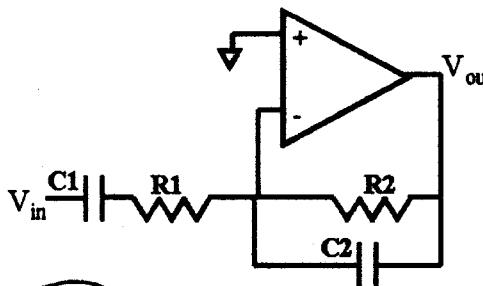
**First 20% Multiple Choice and True/False (Select the most correct answer)**

1.) (4-points) If the MOS capacitor shown in the diagram to the right were used in a MOSFET transistor, which of the following COULD result (more than one answer possible):

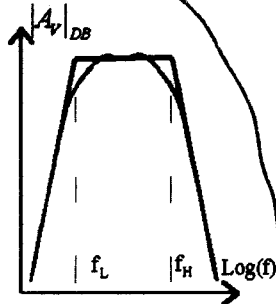
- a.) NMOS transistor biased into Cutoff mode
- b.) PMOS transistor biased into Cutoff mode -
- c.) NMOS transistor biased into linear/triode mode
- d.) PMOS transistor biased into linear/triode mode -
- e.) NMOS transistor biased into Saturation mode
- f.) PMOS transistor biased into Saturation mode -



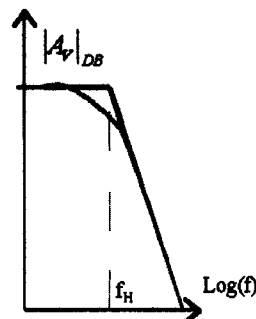
2.) (4-points) The amplifier below has which of the following filter shapes (circle the correct filter shape)?



a)



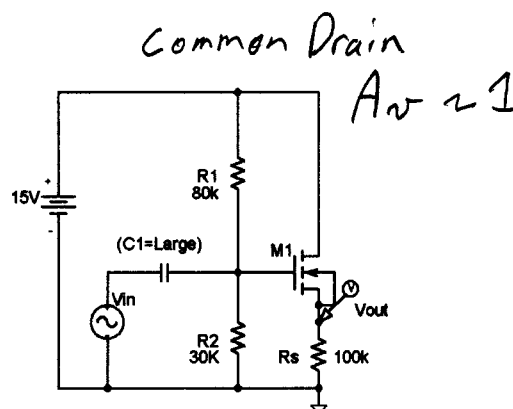
b)



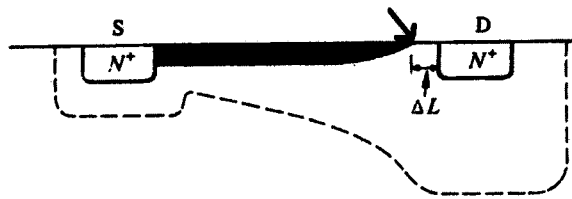
c)

3.) (4-points) In the MOSFET amplifier to the right, WITHOUT doing any calculations, estimate the voltage gain.

- a.)  $g_m R_L$
- b.)  $R_2/R_1 + R_2$
- c.) One
- d.) Can not be determined without  $V_T$

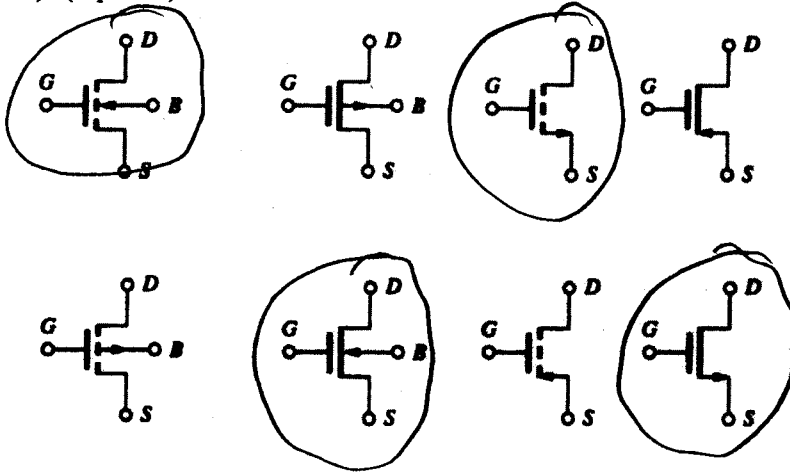


4.) (4-points) In the MOSFET transistor to the right, what is the voltage at the point in the channel indicated by the arrow?



- a.)  $V_{DS} - V_T$
- b.)  $V_{DS}$
- c.)  $V_{GS}$
- d.)  $V_{GS} - V_T$
- e.) Not enough information given to solve
- f.) 1 Volt (because it sounds like a nice round number)

5.) (4-points) Circle ALL the NMOS transistors below...

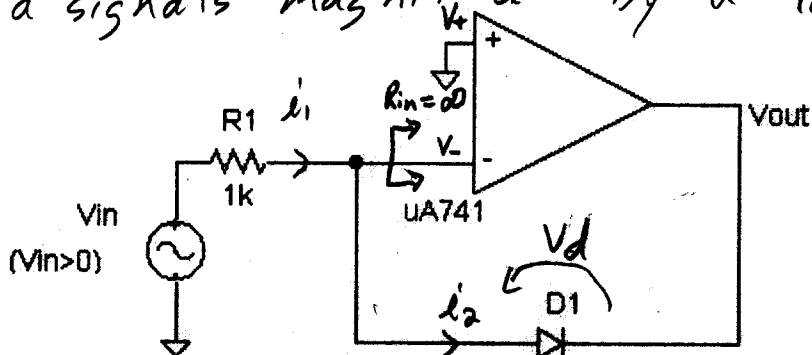


6.) (20-points) In the amplifier circuit below,  $V_{in}$  is constrained to be greater than 0V ( $V_{in} > 0$ ). Determine an expression relating  $V_{out}$  to  $V_{in}$  and determine the input impedance of the circuit. You may assume that the Op-Amp is ideal, the diode has a saturation current of  $1e-14$  A and the CVD model DOES NOT hold (i.e. use the full diode equation). *NOTE: In this circuit, you cannot write a gain expression of the form  $V_{out}/V_{in}$ . You must write an expression relating  $V_{out}$  as a function of  $V_{in}$  ( $V_{out}=f(V_{in})$ ).*

Bonus Points (must be completely correct or no points awarded):

(5 points) Why must  $V_{in}$  be constrained to greater than 0V for this circuit to operate properly?

This is a simplified example of a "compressor" Amplifier that regulates a signals magnitude by a logarithmic function.



$$i_1 = i_2 = I_0 (e^{V_d/V_{th}} - 1)$$

$$V_- = V_+ = 0V$$

$$V_{out} = -V_d$$

$$\therefore i_1 = \frac{V_{in}}{R_1} = I_0 (e^{V_d/V_{th}} - 1)$$

$$V_d = V_{th} \ln \left( \frac{V_{in}}{R_1 I_0} + 1 \right)$$

$$V_{out} = -0.0259 \ln \left( \frac{V_{in}}{1k(1e-14)} + 1 \right)$$

$$V_{out} = -0.0259 \ln (1e11 V_{in} + 1) \text{ V}$$

$$R_{in} = R_1 = 1k\Omega$$

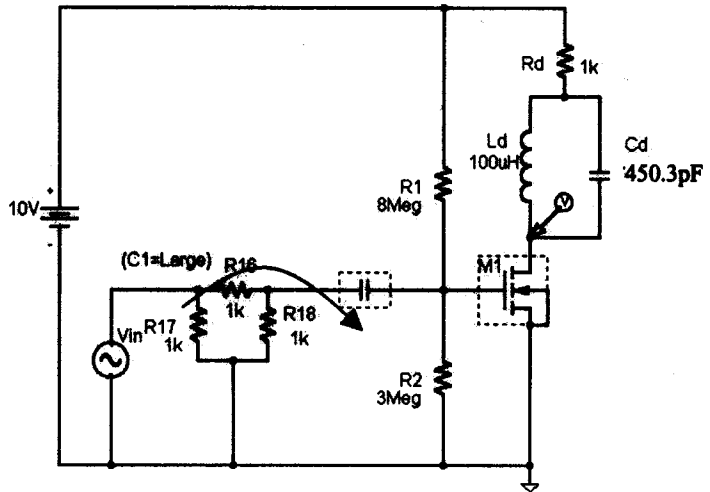
This Problem was <sup>almost</sup> identical to the problem performed in class 3 days before the exam!! See Summer 2002 exam 3.

Pulling all the concepts together for a useful purpose:

7.) (60-points) I want to listen to an Atlanta Braves game on AM 750 radio operating at 750 KHz while rejecting AM 790's station at 790 KHz. In my lab, I build the following "tuned amplifier". Given the following circuit and material parameters, what is the AC voltage gain,  $V_{out}/V_{in}$  at 750KHz and also at 790 KHz (two separate answers)? You may assume capacitor, C1 (and only C1) has an infinite capacitance.

**Both  $L_d$  and  $C_d$  must be considered as finite, complex impedances. "If" your gain is a complex number, show it in magnitude phase form, i.e.  $Mag \angle Degrees$  form.** Additionally, consider the circuit to be operated at low frequencies where you can neglect all small signal capacitances. Grading will be based as such: 16 points for the plug and chug answers, 20 points for DC solution and 24 points for small signal analysis. *Hint: Initially, perform all calculations using a combined impedance,  $Z_d$  ( $Z_d$  includes  $R_d$ ,  $L_d$ ,  $C_d$ ), then substitute the proper value of  $Z_d$  at 750 and 790 KHz in at the end.*

- Gate Length,  $L=2.5 \mu m$
- Gate Width,  $W=Z=25 \mu m$
- Effective mobility,  $\mu_n=200 cm^2/VSec$
- Oxide Thickness,  $t_{ox} = x_{ox} = 27.6 nm$
- Channel Length Modulation parameter,  $\lambda=0.1 V^{-1}$
- Substrate Doping,  $N_A=1.68e16 cm^{-3}$
- Oxide relative Dielectric Constant,  $\epsilon_{r-oxide}=K_O=3.9$
- Substrate relative Dielectric Constant,  $\epsilon_{r-semiconductor}=K_S=11.7$
- Substrate intrinsic concentration,  $n_i=1e10 cm^{-3}$
- Dielectric Constant of free space,  $\epsilon_0 = 8.854e-14 F/cm$



Plug and Chug Answers (As these answers are required to continue on with the problem, you may purchase these answers for 5 points each. Exam proctors should clearly mark values as "PURCHASED"):

$V_T = 1.25 V$        $\phi_F = 0.37 V$

$C_{ox} = 1.25e-7 F/cm^2$        $K_n = 250 \mu A/V^2$

$\phi_F = \frac{kT}{q} \ln\left(\frac{1.68e16}{1e10}\right) = 0.37 V$

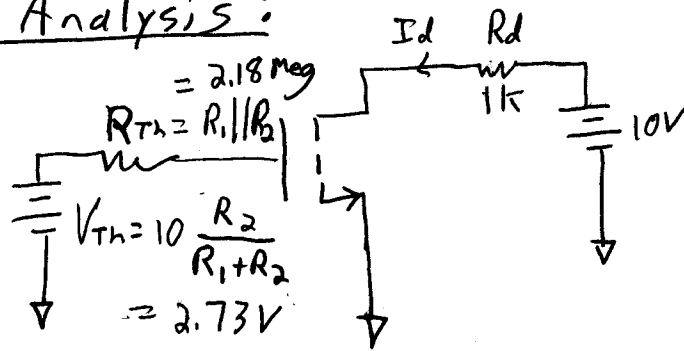
$C_{ox} = \frac{3.9 (8.854e-14)}{2.76e-6} = 1.25e-7 F/cm^2$

$K_n = \left(\frac{25}{2.5}\right) 200 (1.25e-7) = 250 \mu A/V^2$

$V_T = 2(0.37) + \frac{11.7(8.854e-14)}{1.25e-7} \sqrt{\frac{2(1.6e-19)(1.68e16)(2)(0.37)}{11.7(8.854e-14)}}$   
 $= 1.25 V$

Extra work can be done here, but clearly indicate with problem you are solving.

## DC Analysis:



$$V_{GS} = V_{Th} = 2.73 \text{ V}$$

$$V_{DS} + I_D R_D = 10 \text{ V}$$

$$V_{DS} = 10 \text{ V} - I_D R_D$$

Assume Saturation

$$I_D = \frac{250e-6}{2} \underbrace{(2.73 - 1.25)^2}_{V_{GS} - V_T} \underbrace{(1 + 0.1(10 \text{ V} - I_D 1 \text{ k}))}_{V_{DS}}$$

$$I_D = 2.738e-4 (1 + 1 - 100 I_D)$$

$$0 = 5.476e-4 - 1.02738 I_D$$

$$I_D = 533 \mu\text{A}$$

Check:

$$V_{DS} = 10 - 10 \text{ k} (533e-6)$$

$$= 4.67 \text{ V} > V_{GS} - V_T = (2.73 - 1.25) = 1.53$$

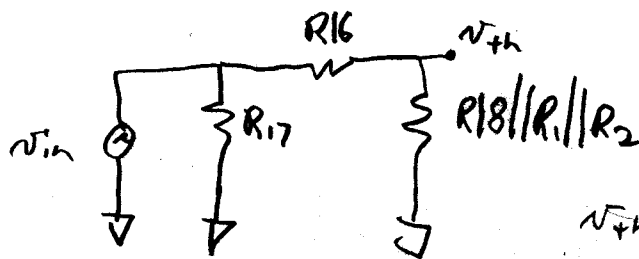
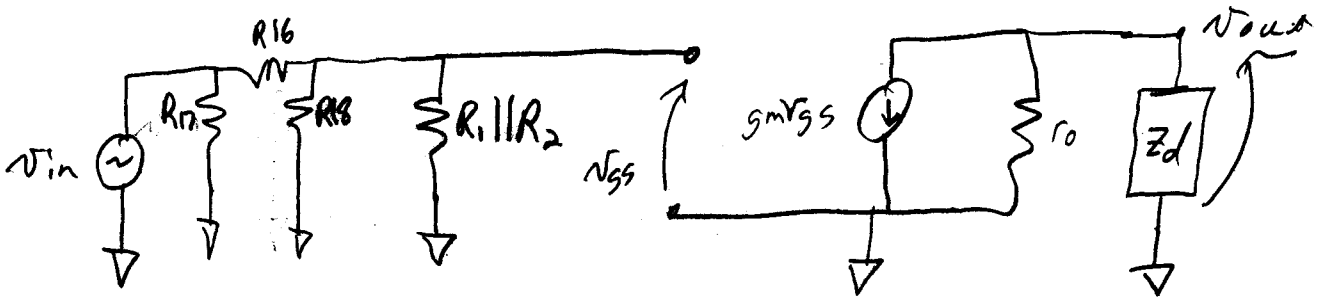
Small signal Parameters ✓ Saturation Verified.

$$g_m = \frac{I_{DS}}{\left(\frac{V_{GS} - V_T}{2}\right)} = \frac{533e-6}{(2.73 - 1.25)/2} = 720 \mu\text{A/V}$$

$$r_o = \frac{1}{\frac{\lambda \kappa_n}{2} (V_{GS} - V_T)^2} = 36.5 \text{ k}\Omega$$

Extra work can be done here, but clearly indicate with problem you are solving.

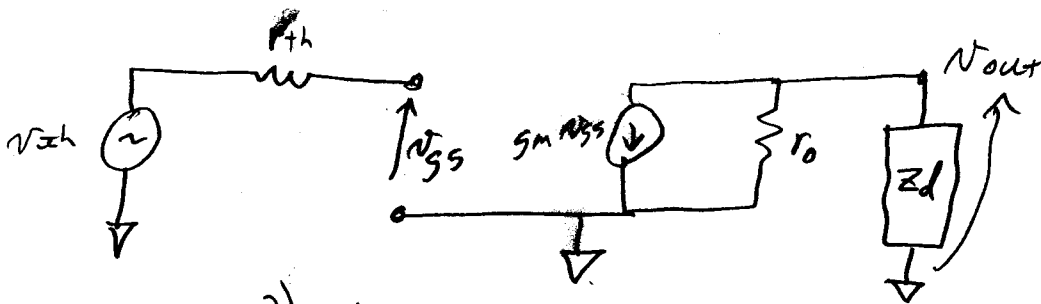
# AC Solution



$$v_{th} = \frac{R18 \parallel R1 \parallel R2}{R18 \parallel R1 \parallel R2 + R16} v_{in}$$

1)  $v_{th} = 0.5 v_{in}$

$R_{th} = R16 \parallel R18 \parallel R1 \parallel R2$   
 $= 500 \Omega$



2)  $v_{gs} = v_{th}$

3)  $v_{out} = -g_m v_{gs} (r_o \parallel z_d)$

$$A_v = \left( \frac{v_{out}}{v_{gs}} \right) \left( \frac{v_{gs}}{v_{th}} \right) \left( \frac{v_{th}}{v_{in}} \right)$$

$$= -g_m (r_o \parallel z_d) (1) (0.5)$$

$$= -g_m \frac{r_o \parallel z_d}{2}$$

what is  $z_d$  @  $f = 750 \text{ kHz}$ ?



Extra work can be done here, but clearly indicate with problem you are solving.

Z<sub>d</sub>:

$$Z_d = \left[ (Ls) \parallel \left( \frac{1}{Cs} \right) \right] + R_d$$

$$= \frac{Ls}{Cs} \parallel \frac{1}{Cs} + R_d$$

$$= \frac{Ls}{1 + s^2 LCd} + R_d$$

$$= \frac{j\omega Ld}{1 + \omega^2 LCd} + R_d$$

Consider 3 cases

$f_1 = 750 \text{ kHz}$

$\omega_1 = 2\pi f_1 = 4,712,388.98$

$j(\omega_1)(100e-6)$

$Z_d = 1k + \frac{j(\omega_1)(100e-6)}{1 - \omega_1^2(100e-6)(450.3e-12)}$

$= 1k + j 0.1423 \omega_1$

$= 1k + j 670,751 \Omega$

$r_{oll} z_d = 36,389 + j 1980$

$A_v = -360e-6 (r_{oll} z_d)$

$= -13.10 - j 0.7128 \text{ V/V}$

$A_v = +13.12 \angle 183.1^\circ \text{ V/V}$

$f_2 = 790 \text{ kHz}$

$\omega_2 = 2\pi f_2$

$Z_d = 1k + \frac{j\omega_2(100e-6)}{1 - \omega_2^2(100e-6)(450.3e-12)}$

note minus

$= 1k + j\omega_2 0.000913$

$= 1k - j 4534.28 \Omega$

$r_{oll} z_d = 1485.25 - j 4233.77$

$A_v = -360e-6 (r_{oll} z_d)$

$= -0.53 + j 1.524$

$A_v = 1.62 \angle 109.3 \text{ V/V}$

$\omega = 2\pi f = \sqrt{\frac{1}{LC}} \text{ @ resonance}$

$\omega^2 LCd = 1$

$Z_d = \frac{j\omega Ld}{0} + R_d$

$Z_d = 100$

$A_v = \frac{-720e-6 (36.5k \parallel 100)}{2}$

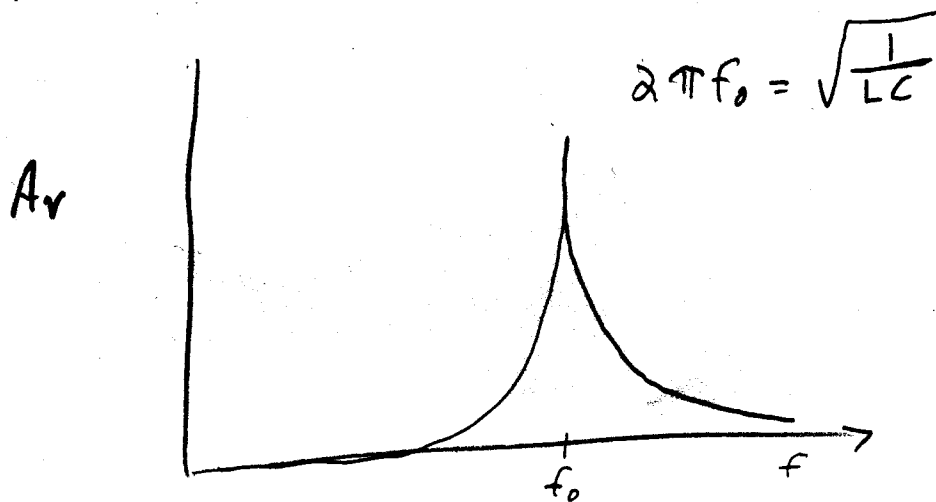
$A_v = -13.14 \text{ V/V}$

(Requires  $f = \frac{1}{2\pi} \sqrt{\frac{1}{LC}}$   
 or  $f = 750,013.6 \text{ Hz}$ )

I.E. Very sensitive  
 Gain-freq. relationship

Extra work can be done here, but clearly indicate with problem you are solving.

This "tuned amplifier" is an example of a bandpass filter often used in Radio + TV circuits (greatly simplified version, of course). It has a frequency response that looks like this.



In a "real implementation",  $R_d$  would be eliminated to increase the freq. selectivity.