

# ECE 3040 Microelectronic Circuits

Exam 3

60 exam 5  
#s 6, 22, 23 not used

*April 15, 2004, Tax payment day!*

*One day soon, you will join the work force and be forced at the point of a gun to give the government 50% of what you earn (local, state, federal, social security and sales taxes)! That goal is what you are studying so hard for! Negotiate a good salary-you will need it!*

*Dr. W. Alan Doolittle*

*Solution*

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Print your name clearly and largely:

**Instructions:**

Read all the problems carefully and thoroughly before you begin working. You are allowed to use 1 new sheet of notes (1 page front and back), your note sheet from the previous exams as well as a calculator. There are 100 total points in this exam. Observe the point value of each problem and allocate your time accordingly. **SHOW ALL WORK AND CIRCLE YOUR FINAL ANSWER WITH THE PROPER UNITS INDICATED.** Write legibly. If I cannot read it, it will be considered a wrong answer. Do all work on the paper provided. Turn in all scratch paper, even if it did not lead to an answer. Report any and all ethics violations to the instructor. Good luck!

Sign your name on **ONE** of the two following cases:

I DID NOT observe any ethical violations during this exam:

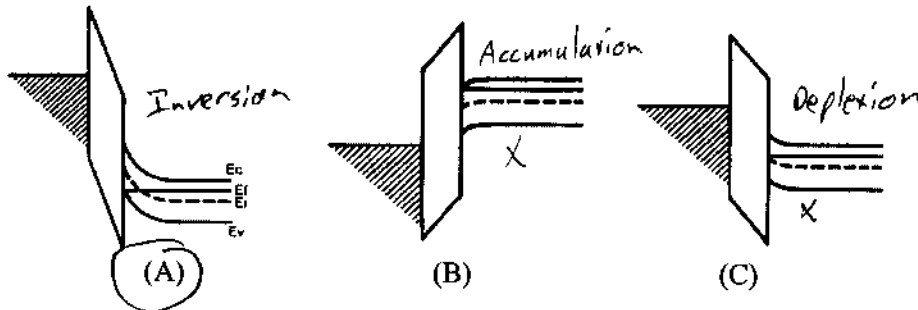
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I observed an ethical violation during this exam:

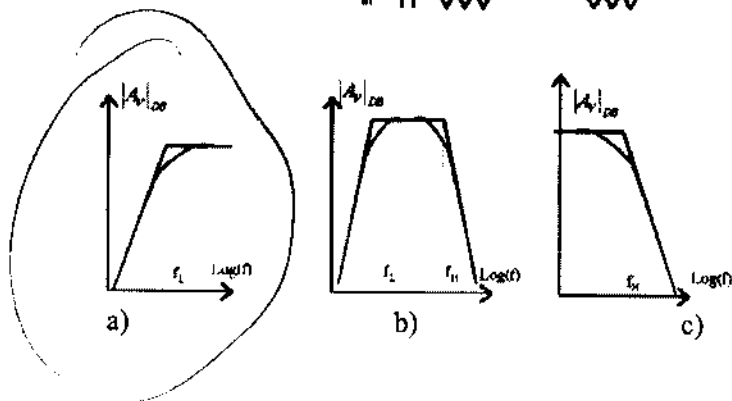
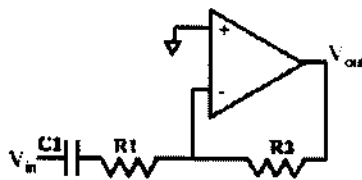
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**First 25% Multiple Choice and True/False (Select the most correct answer)** (5)

1.) (5-points) A MOS capacitor is used on an enhancement mode MOSFET. When the MOSFET is conducting current, which of the following energy band diagrams would be correct for the MOS capacitor.

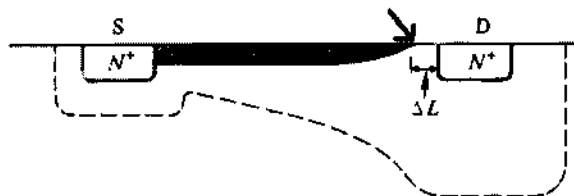


2.) (5-points) The amplifier below has which of the following filter shapes (circle the correct filter shape)?

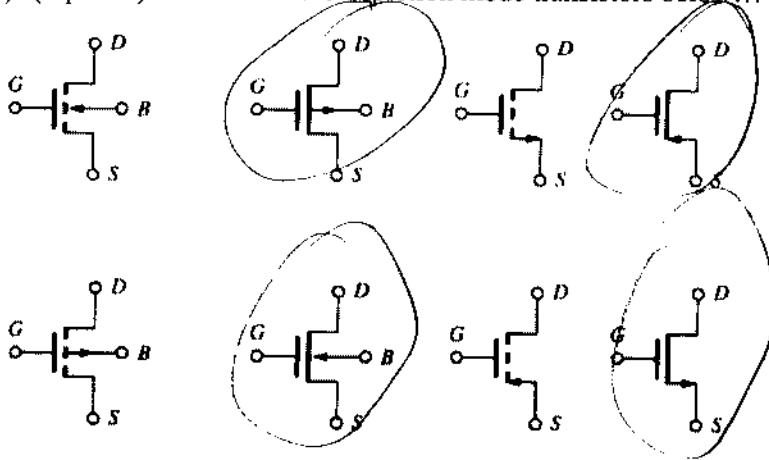


3.) (5-points) In the MOSFET transistor to the right, what is the voltage at the point in the channel indicated by the arrow?

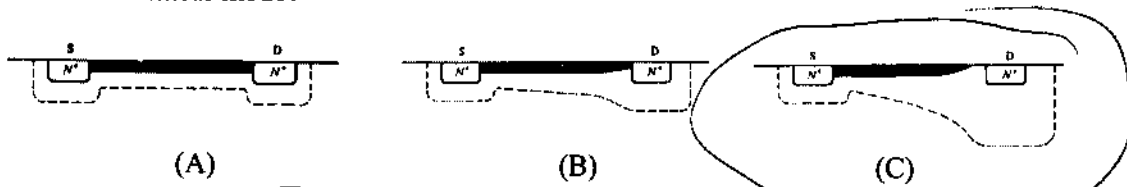
- a.)  $V_{DS} - V_T$
- b.)  $V_{DS}$
- c.)  $V_{D, \text{sat}}$
- d.)  $V_{GS} - V_T$
- e.) Not enough information given to solve



4.) (4-points) Circle ALL the depletion mode transistors below ...



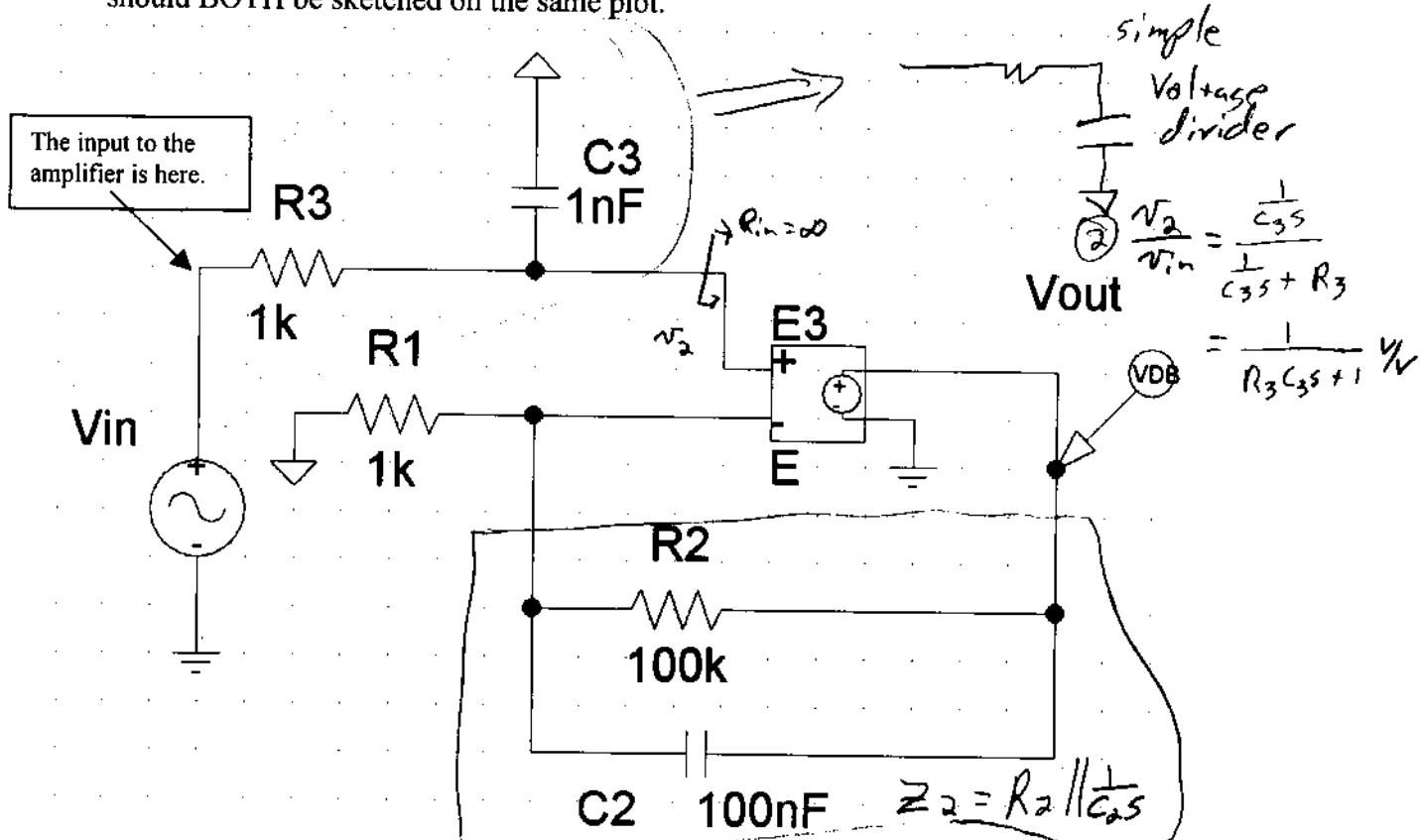
5.) (3-points) Which of the following MOSFET cross sectional views is NOT of a transistor biased in linear mode?



6.) (3-points) True/False Gauss's law as applied to the oxide-semiconductor interface states that the electric field in the semiconductor is equal to the electric field in the oxide.

Second 25% Problems (3<sup>rd</sup> 25%)

7.) (15-points) Sketch and label all break frequencies, the voltage gain in flat regions in a Bode plot (gain in dB vs Log(frequency)). You may assume that the Op-Amps are ideal. To receive full credit the asymptotes and an estimate of the actual gain curve should BOTH be sketched on the same plot.



Non inverting stage:

$$\textcircled{1} \frac{V_{out}}{V_2} = 1 + \frac{R_2}{R_1} \Rightarrow \frac{V_{out}}{V_2} = 1 + \frac{R_2 \parallel \frac{1}{C_2 s}}{R_1}$$

$$= 1 + \frac{R_2 \frac{1}{C_2 s}}{R_2 + \frac{1}{C_2 s}} = \frac{R_2}{R_2 C_2 s + 1} + \frac{R_1}{R_1} \left[ \frac{(R_2 C_2 s + 1)}{(R_2 C_2 s + 1)} \right]$$

$$= \frac{R_2 + R_1 + R_1 R_2 C_2 s}{(R_2 C_2 s + 1) R_1}$$

$$\frac{V_{out}}{V_2} = \left[ 1 + \frac{R_2}{R_1} \right] \frac{R_1 (1 + R_2 C_2 s)}{1 + (R_1 \parallel R_2) C_2 s} \text{ V/V}$$

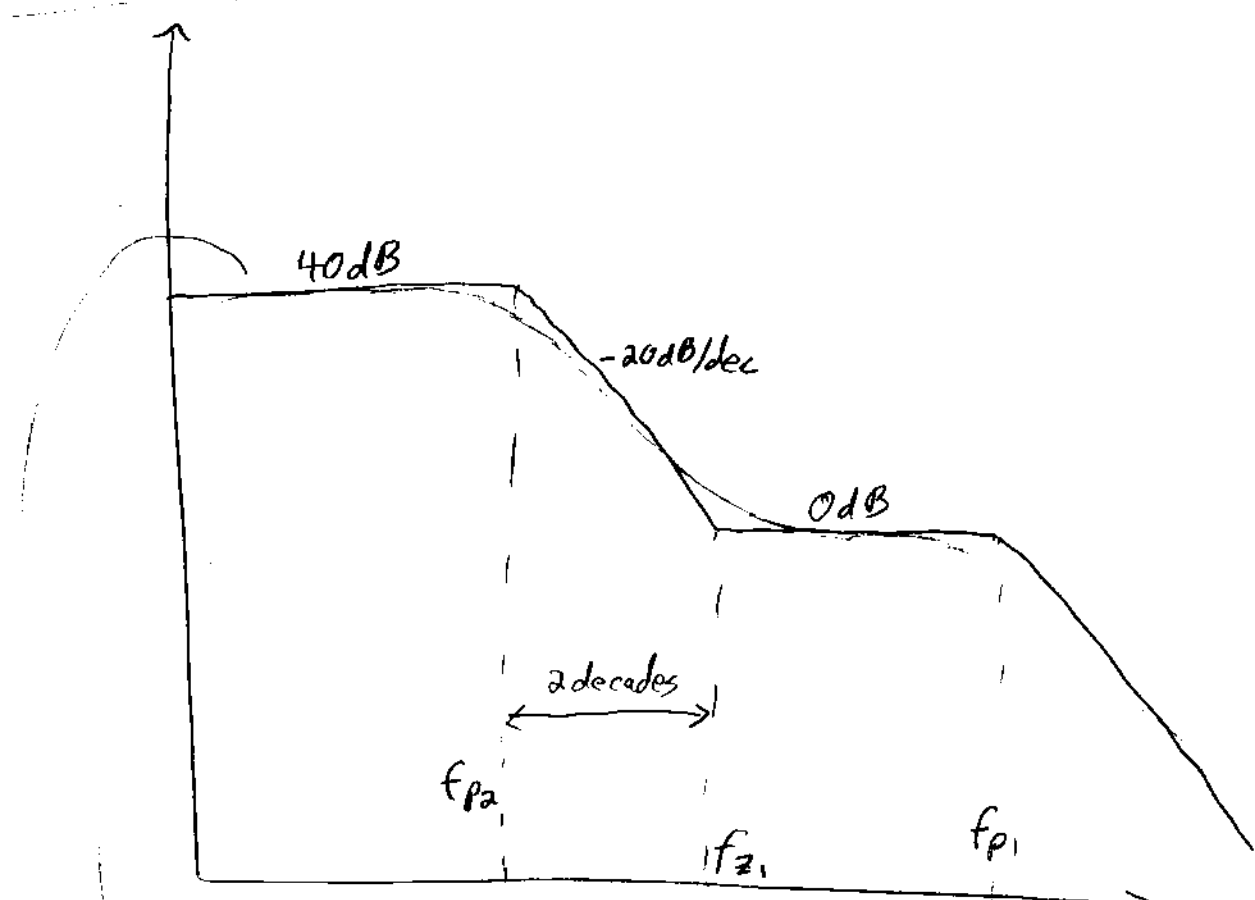
Extra work can be done here, but clearly indicate with problem you are solving.

$$\frac{V_{out}}{V_{in}} = \frac{\textcircled{1}}{V_2} \frac{\textcircled{2}}{V_1} = \frac{1}{R_3 C_3 s + 1} \left( \frac{1 + R_1 \parallel R_2 C_2 s}{1 + R_2 C_2 s} \right) \left( 1 + \frac{R_2}{R_1} \right)$$

$$f_{p1} = \frac{1}{2\pi R_3 C_3} = 159 \text{ kHz}$$

$$f_{z1} = \frac{1}{2\pi (R_1 \parallel R_2) C_2} = 1.6 \text{ kHz}$$

$$f_{p2} = \frac{1}{2\pi R_2 C_2} = 15.9 \text{ Hz}$$



$$20 \log \left( 1 + \frac{100 \text{ k}\Omega}{1 \text{ k}\Omega} \right) = 40.08 \text{ dB}$$

$$A_v = 101 \text{ V/V}$$

8.) (10-points) The same circuit for problem 7 is used with an Op-Amp designed by a Clemson engineer. This Op-Amp only has an open loop gain of  $A_{v\text{-openloop}}=1000$  V/V,  $R_{in}=10K$  ohms, and  $R_{out}=500$  ohms. ATDC (i.e. open circuit any capacitors) determine the voltage gain, input resistance and output resistance of the circuit. DO NOT USE THE IDEAL OP-AMP ASSUMPTION. Hint: you may find it helpful to determine the feedback factor,  $\beta$  as part of your solution.

$$A_{v\text{closed-loop}} = \frac{A_{v\text{openloop}}}{1 + \beta A_{v\text{openloop}}}$$

$$= \frac{1000}{1 + \frac{1}{101}(1000)}$$

$$\beta = \frac{R_1}{R_1 + R_2}$$

$$= \frac{1k}{100k + 1k} = \frac{1}{101}$$

$$A_{v\text{closed-loop}} = 91.73 \text{ V/V}$$

$$R_{in\text{-closed-loop}} = (1 + \beta A_{v\text{openloop}}) R_{in\text{-open}} + R_3$$

$$= \left[ 1 + \frac{1}{101}(1000) \right] 10K + 1K$$

$$R_{in\text{-closed}} = 110 \text{ K } \Omega$$

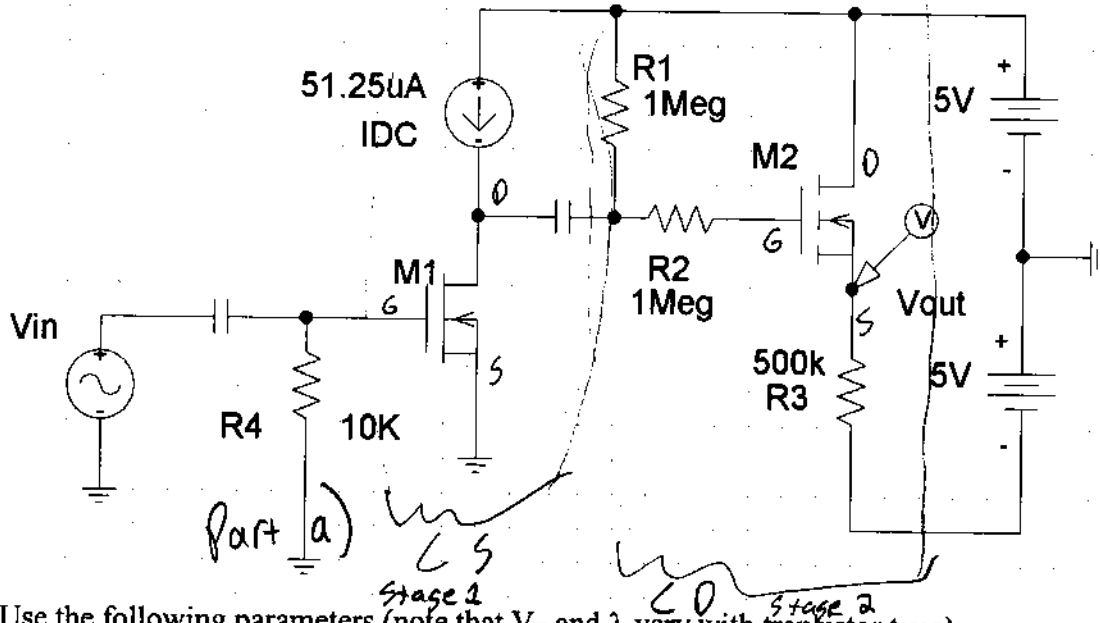
$$R_{out\text{closed}} = \frac{R_{out\text{open}}}{1 + \beta A_{v\text{openloop}}} \parallel (R_1 + R_2)$$

$$= \frac{500}{\left[ 1 + \frac{1}{101}(1000) \right]}$$

$$R_{out\text{-closed}} = 45.9 \text{ } \Omega$$

Pulling all the concepts together for a useful purpose: (4<sup>th</sup> 25%)

9.) **(50-points)** Given the following circuit, (a) Identify the configuration of the two stages (common \_\_\_\_). (b) What is the AC voltage gain,  $V_{out}/V_{in}$ ? You may assume all capacitors have infinite capacitance and are thus, AC shorts. Additionally consider the circuit to be operated at low frequencies where you can neglect all small signal capacitances. Grading will be based as such: part a=5 points, part b=18 points for DC solution (gate, source and drain voltages along with drain current), 9 points for the conversion to the small signal model and 18 points for small signal analysis.



Use the following parameters (note that  $V_T$  and  $\lambda$  vary with transistor type):

M1 For NMOS Depletion Transistors:  
 $K_n' = 25 \mu A/V^2$   $V_T = -2.0V$   $\lambda = 0.01 V^{-1}$  Length (L)=10  $\mu m$  Width (W)=10  $\mu m$

M2 For NMOS Enhancement Transistors:  
 $K_n' = 25 \mu A/V^2$   $V_T = +0.75V$   $\lambda = 0.0 V^{-1}$  Length (L)=10  $\mu m$  Width (W)=10  $\mu m$

For PMOS Depletion Transistors:  
 $K_n' = 25 \mu A/V^2$   $V_T = +3.0V$   $\lambda = 0.01 V^{-1}$  Length (L)=10  $\mu m$  Width (W)=10  $\mu m$

For PMOS Enhancement Transistors:  
 $K_n' = 25 \mu A/V^2$   $V_T = -1.75V$   $\lambda = 0.0 V^{-1}$  Length (L)=10  $\mu m$  Width (W)=10  $\mu m$

DC M1 Assume Saturation:

$$51.25 \mu A = \frac{25e-6}{2} \left(\frac{10}{10}\right) (V_{GS1} - (-2.0))^2 (1 + 0.01 V_{DS1})$$

but  $V_{GS1} = 0$

$$51.25e-6 = 50e-6 (1 + 0.01 V_{DS1})$$

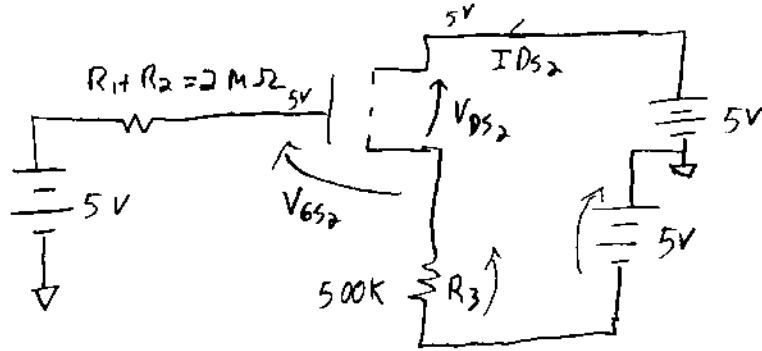
$$V_{DS1} = 2.5V$$

check assumption:

$$V_{DS1} > V_{GS1} - V_T \Rightarrow 2.5V > 0 - -2$$

$$2.5V > 2V \checkmark$$

Extra work can be done here, but clearly indicate with problem you are solving.



Assume Saturation:  $\left(\frac{10}{10}\right)$

$$I_{DS2} = \frac{\mu_n'}{2} \left(\frac{W}{L}\right) (V_{GS2} - V_T)^2 (1 + \lambda V_{DS2})$$

$$= \frac{2.5e-5}{2} (V_{GS2} - 0.75)^2$$

$$5V - V_{DS2} - I_{DS2} R_3 + 5V = 0 \Rightarrow \text{does not help}$$

$$5V = V_{GS2} + R_3 I_{DS2} - 5V$$

$$I_{DS2} = 12.5e-6 (10V - R_3 I_{DS2} - 0.75)^2 = 1.25e-5 (9.25 - R_3 I_{DS2})^2$$

$$= 1.25e-5 (9.25^2 - 18.5 I_{DS2} R_3 - R_3^2 I_{DS2}^2)$$

$$= 0.001069 - 115.625 I_{DS2} - 3.125e6 I_{DS2}^2$$

$$3.125e6 (I_{DS2})^2 + 116.625 I_{DS2} - 0.001069 = 0$$

$$I_{DS2} = \frac{-116.625 \pm \sqrt{(116.625)^2 - 4(3.125e6)(0.001069)}}{2(3.125e6)}$$

$$= 21.1 \mu A$$

or

$$I_{DS2} = 16.22 \mu A$$

$$V_{GS} = 10V - R_3 I_{DS2} = -0.55V \text{ or } 1.89V$$

check assumption:  $V_{DS} > V_{GS} - V_T$

$$V_{DS} = V_{GS} \Rightarrow 0 > -0.75V \checkmark \checkmark$$



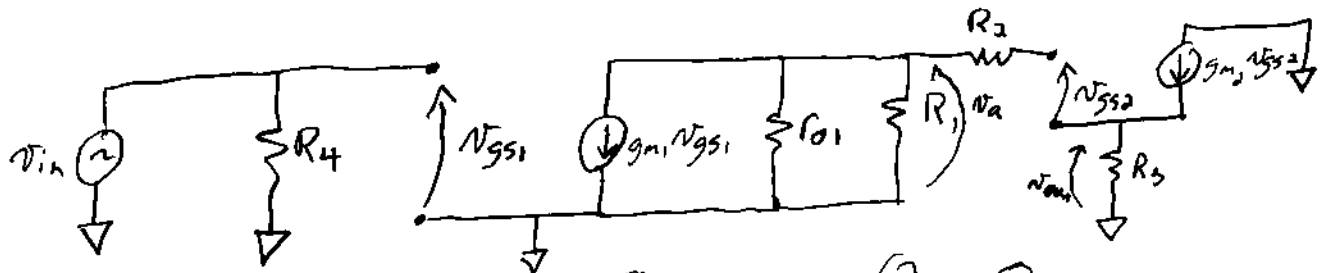
Extra work can be done here, but clearly indicate with problem you are solving.

Conversion:  $g_{m1} = \frac{51.25e-6}{0 - (-2.0)} = 51.25e-6 \Omega^{-1}$

$$r_{o1} = \frac{\frac{1}{\lambda} + V_{DS1}}{I_{DS1}} = \frac{100 + 2.5}{51.25e-6} = 2e6 \Omega$$

$$g_{m2} = \frac{16.22 \mu A}{\left(\frac{1.89 - 0.75}{2}\right)} = 28.45e-6 \Omega^{-1}$$

$$r_{o2} = \infty \text{ (since } \lambda = 0\text{)}$$



AC solution

$$A_v = \frac{v_{out}}{v_{in}} = \frac{v_{out}}{v_{gss2}} \textcircled{1} \left(\frac{v_{gss2}}{v_a}\right) \textcircled{2} \left(\frac{v_a}{v_{gss1}}\right) \textcircled{3} \left(\frac{v_{gss1}}{v_{in}}\right) \textcircled{4}$$

$$\textcircled{1} \quad v_{out} = g_{m2} v_{gss2} R_3$$

$$\frac{v_{out}}{v_{gss2}} = (28.45e-6)(500e3) = 14.225 \text{ V/V}$$

$$\textcircled{2} \quad v_a = v_{gss2} + g_{m2} v_{gss2} R_3$$

$$\frac{v_{gss2}}{v_a} = \frac{1}{1 + g_{m2} R_3} = \frac{1}{1 + (28.45e-6)(500e3)} = 0.06588$$

$$\textcircled{3} \quad v_a = -g_{m1} v_{gss1} (r_{o1} \parallel R_1)$$

$$\frac{v_a}{v_{gss1}} = -g_{m1} (r_{o1} \parallel R_1) = -51.26e-6 (2e6 \parallel 1e6) = -34.16 \text{ V/V}$$

$$\textcircled{4} \quad v_{gs1} = v_{in}$$

$$\frac{v_{gs1}}{v_{in}} = 1$$

$$A_v = (14.225)(0.06568)(-34.16)(1)$$

$$A_v = -31.9 \text{ V/V}$$