



Neuromorphic Computing

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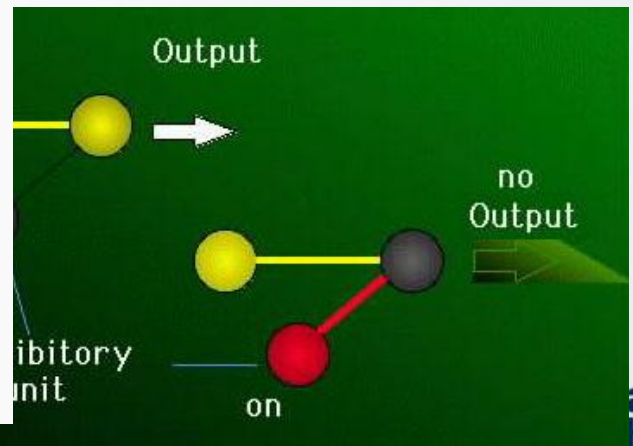
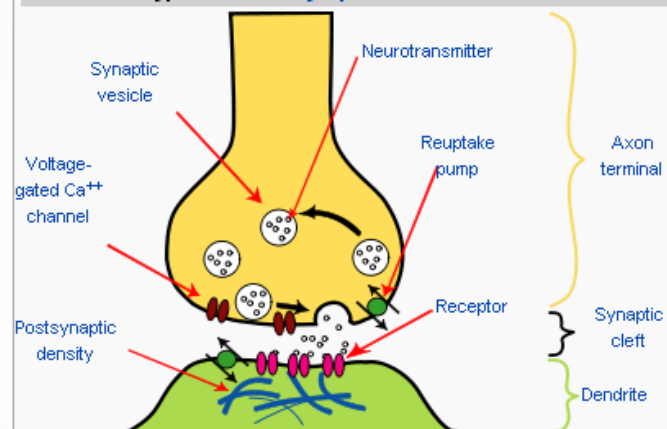
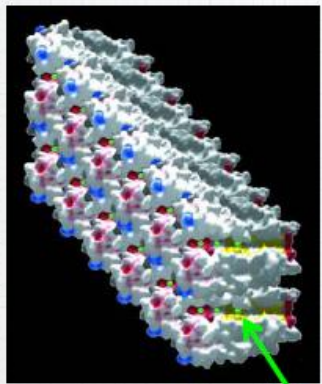
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We Will Be Looking at...

- Basic concept of neuromorphic computing
- Incentive behind neuromorphic computing
- Facilitation of technology
 - Architecture of Neurogrid
 - Neuromorphic vision chips
 - Complementary Oxide Memristor
- Barriers preventing advancement

First...

- Neuron - axon and dendrite
- Synapses:
 - Chemical/Electrical
 - Inhibitory synapse response
 - Excitatory synapse response



What is neuromorphic computing?

- How information is transferred in the brain:
 - Neurons
 - Ion Channel
 - Neural Networks
- Parallel neural networks

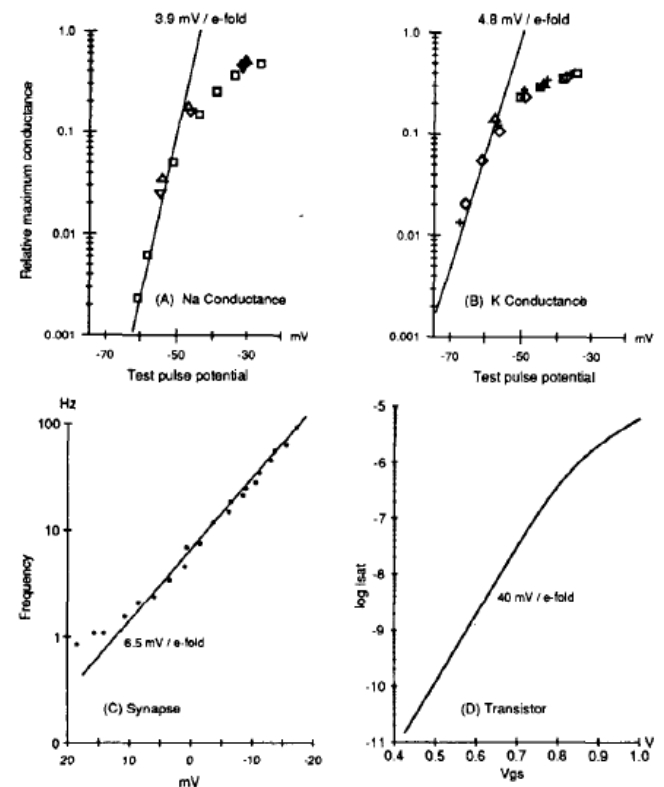
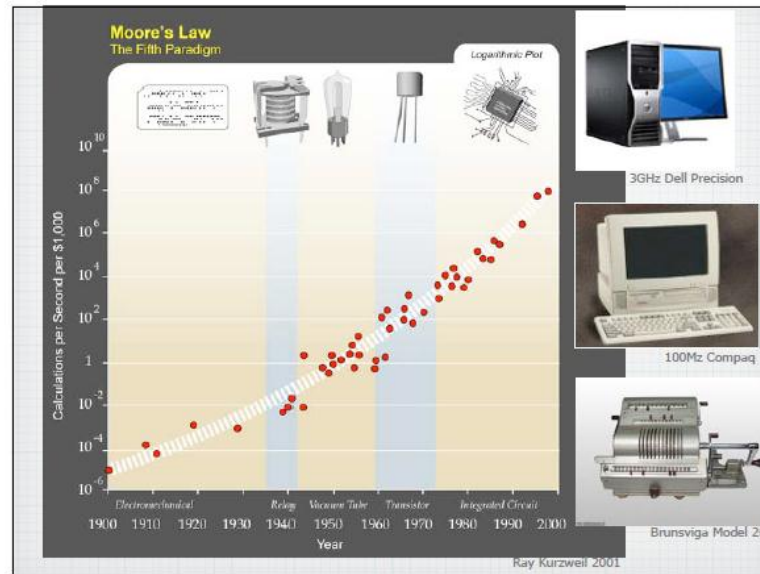


Fig. 1. Current-voltage plots for several important devices.

Advantages

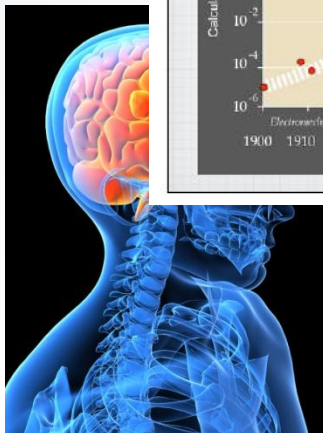
- Modern integrated circuits cannot downsize anymore

- Less power
 - 200,000
 - 10 W —



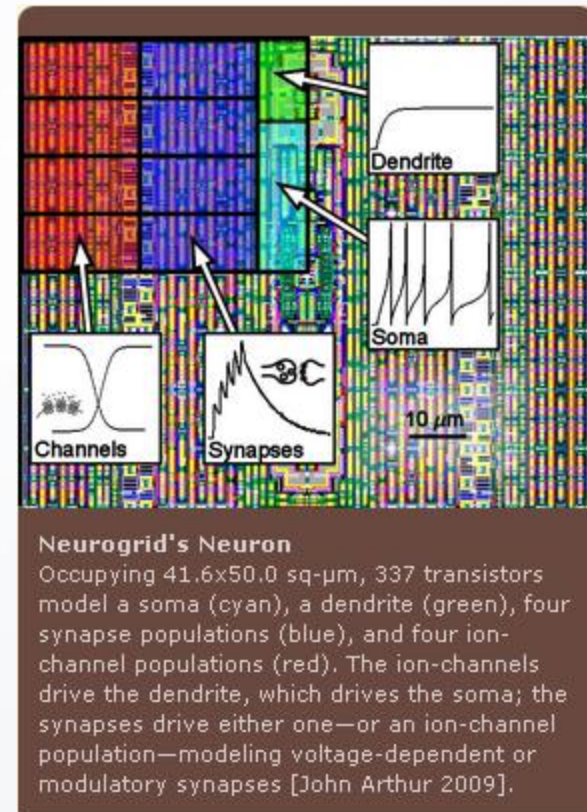
computing

ns/s



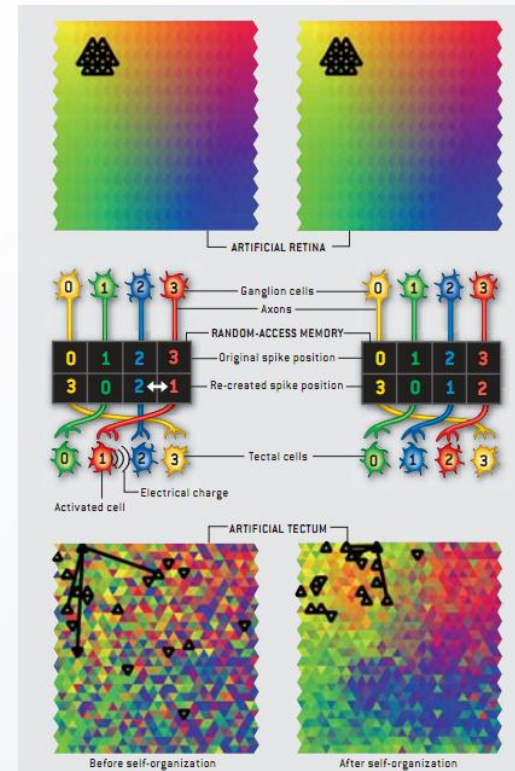
Neurogrid

- Simulation of biological brain
- Analog computation
- Digital communication



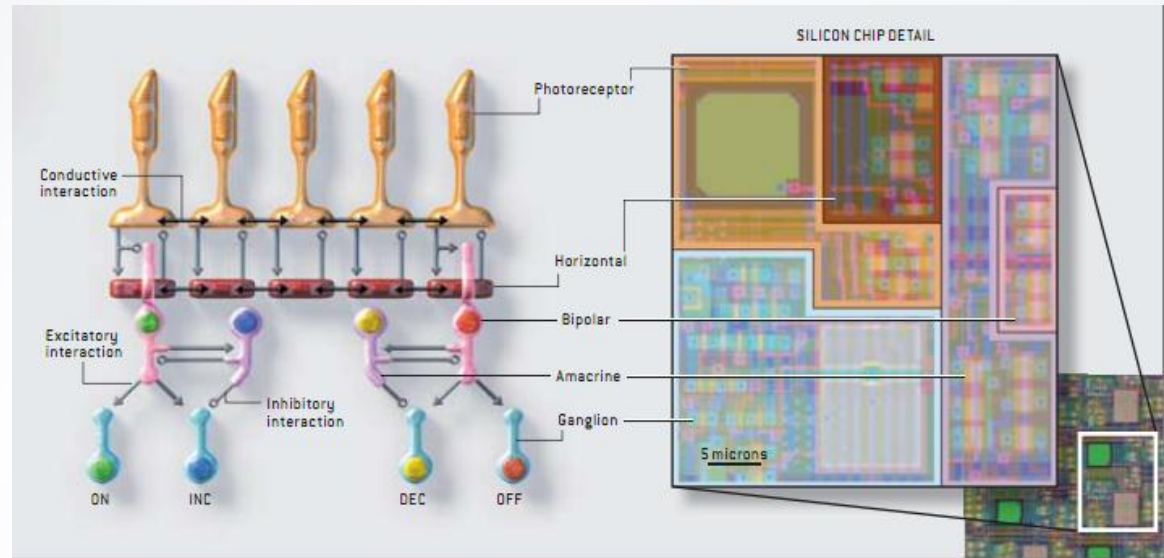
Vision Chips

- Simulation of retina in silicon chip
- How visual field is interpreted to the brain
- Emulation of the retina



The challenge

- Retina cell
 - 0.5 mm thickness
 - 0.5 grams
 - 0.1 W



Using memristors

- Able to emulate excitatory/inhibitory synapse
- Change in voltage – rapid recovery of resistance
- Maintain resistance

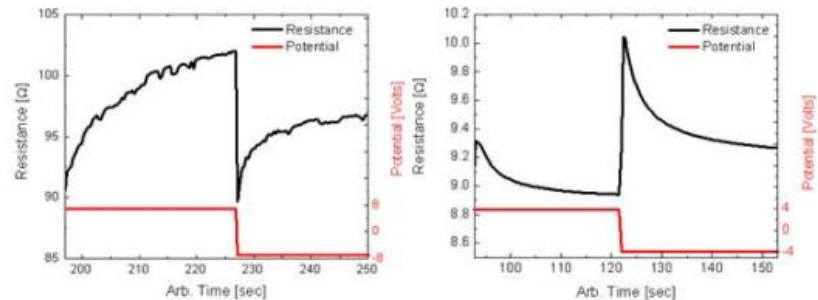
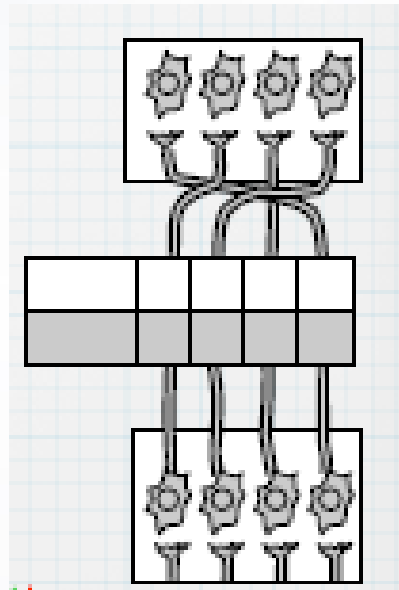


Figure 1 Large 40 μm LiNbO_2 memristors are the only known macroscopic demonstration of memristance. Having both n-type (left) and p-type (right) operation allows memristors that have complementary behavior (increase/decreasing resistance with applied voltage) and suggests the ionic/electronic equivalent of CMOS circuitry may be possible. Resistors show minimal hysteresis and zero set/reset voltage (i.e. a truly analog memristor).

Work in progress...

- Today's computers rely on serial connections
- Relatively new branch for computing



Sources

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