

# ECE 4813

# Semiconductor Device and Material Characterization

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As with all of these lecture slides, I am indebted to Dr. Dieter Schroder from Arizona State University for his generous contributions and freely given resources. Most of (>80%) the figures/slides in this lecture came from Dieter. Some of these figures are copyrighted and can be found within the class text, *Semiconductor Device and Materials Characterization*. <u>Every serious</u> *microelectronics student should have a copy of this book!* 



# **Contact Resistance Metal-semiconductor Contacts Specific Contact Resistance Transfer Length Contact String Transfer Length Method Four-terminal Method**





### Metal – Semiconductor Contacts

- Every semiconductor device has contacts
- Contact resistance is a parasitic resistance
- Contacts are almost always metal-semiconductor contacts







## Metal – Semiconductor Contacts

- Energetic barriers are most often described by the "Electron Affinity Model" (EAM).
- EAM is a purely theoretical model and <u>REAL CONTACTS</u> <u>RARELY FOLLW THIS MODEL</u>.
- Nevertheless, EAM explains several aspects (depletion widths, capacitance, general IV shape, etc...) of ohmic and Schottky barrier diodes sufficiently that it is widely used.
- Reasons for the barrier height not agreeing with theory:
  - Image force lowering (lower than expected barrier)
  - Fermi-level pinning
  - Interface state filling (i.e. partially pinned or voltage dependent interface state occupation).
  - Lack of complete model understanding. Can you improve on the existing model?



### **Carrier Motion**

In an ohmic contact, electrons must flow from metal to semiconductor or from semiconductor to metal



Current is controlled by barrier width rather than barrier height



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#### Energy band diagrams for ideal MS Georgia Tech Contacts







- After the contact formation, electrons will begin to flow from the semiconductor to the metal.
- The removal of electrons from the n-type material leaves behind uncompensated N<sub>d</sub><sup>+</sup> donors, creating a surface depletion layer, and hence a built-in electric field (similar to p<sup>+</sup>-n junction).
- Under equilibrium, the Fermi-level will be constant and no energy transfer (current) flows
- A barrier  $\Phi_{\rm B}$  forms blocking electron flow from M to S.
- Based on the Electron Affinity Model (EAM), the simplest of models used to describe MS junctions,  $\Phi_B = \Phi_M \chi$  ... ideal MS (n-type) contact.  $\Phi_B$  is called the "barrier height".
- Electrons in a semiconductor will encounter an energy barrier equal to  $\Phi_{M} \Phi_{S}$  while flowing from S to M.



Since MS Schottky diode is a <u>majority carrier device (i.e only majority carriers are</u> <u>injected from semiconductor to the metal)</u> and thus has no minority carrier storage, the frequency response of the device is much higher than that of equivalent  $p^+$  n diode.

The "turn on voltage" of a Schottky diode is typically smaller than a comparable p-n junction since the barrier to forward current flow  $(\Phi_m - \Phi_s)$  is typically small. This "turn on" voltage can be as small as 0.3 Volts in some Si Schottky diodes.

This makes a Schottky diode the best choice for power switch protection in inductive load applications (motors, solenoids, coils, etc...) and in high frequency rectification but not a good choice when low leakage or high breakdown voltage is required.



Leakage in a Schottky diode is dominated by:

- 1) "Thermionic Emission" (metal electrons emitted over the barrier not likely)
- 2) "Thermionic Field Emission" (metal electrons of higher energy tunneling through the barrier more likely)
- 3) "Direct tunneling" (metal electrons tunneling through the barrier most likely in higher doped semiconductors or very high electric fields).
- Since generation does not require the entire bandgap energy to be surmounted, the reverse leakage current for a Schottky diode is generally much larger than that for a p<sup>+</sup>n diode. Likewise, breakdown (for the same reason) is generally at smaller voltages.

### MS (n-type) contact with $\Phi_{M} > \Phi_{S}$

A forward bias will reduce the barrier height unbalancing the electron current flow, resulting in a huge forward current that increases exponentially with applied voltage

A reverse bias will increase the barrier height resulting in a small "reverse current" flow that will be dominated by tunneling currents for high doped semiconductors and/or thermally assisted field emission for moderate/low doped semiconductors.



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*n*-type

semiconductor

Metal

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# Ohmic Contacts: MS (n-type) contact with $\Phi_{M} < \Phi_{S}$

- There is no barrier for electron flow from the semiconductor to the metal. So, even a small  $V_A > 0$  results in large current.
- The small barrier that exists for electron flow from metal to the semiconductor, but vanishes when  $V_A < 0$  is applied to the metal. Large current flows when  $V_A < 0$ .
- The MS (n-type) contact when Φ<sub>M</sub> < Φ<sub>S</sub> behaves like an ohmic contact.
- Lack of depletion (accumulation occurs) means (essentially) no rectification.



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### Generalization of Metal Semiconductor Contact Energy Relationships

	n-type	p-type
$\Phi_{\rm M} > \Phi_{\rm S}$	rectifying	ohmic
$\Phi_{\rm M} < \Phi_{\rm S}$	ohmic	rectifying



### **Carrier Motion**

n-type and p-type substrates are analogs of one another





### **Specific Contact Resistivity**

- The specific contact resistivity,  $\rho_c$ , is an area-independent parameter

$$\rho_c = \left(\frac{dJ}{dV}\right)_{V=0}^{-1}$$

Thermionic emission

$$J = A * T^{2} e^{-q\phi_{B}/kT} \left( e^{qV/kT} - 1 \right)$$
$$\rho_{c} = \frac{k}{qA * T} e^{q\phi_{B}/kT} = \rho_{1} e^{q\phi_{B}/kT}$$

• For  $\phi_B = 0.6$  V,  $A^* = 110$  A/cm<sup>2</sup>K<sup>2</sup>, T = 300 K

$$\rho_c = 1600 \ \Omega\text{-cm}^2 \implies R_c = 1.6 \times 10^{11} \ \Omega \text{ !!}$$
$$(A = 1 \ \mu\text{mx}1 \ \mu\text{m})$$



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### **Specific Contact Resistivity**

Thermionic emission

 $\rho_c = \rho_1 \mathbf{e}^{q\phi_B/kT}$ 

Thermionic-field emission

$$E_o = E_{oo} \operatorname{coth} E_{oo} / kT$$

$$\boldsymbol{E}_{oo} = \frac{\boldsymbol{qh}}{4\pi} \sqrt{\frac{\boldsymbol{N}_{D}}{\boldsymbol{K}_{s} \boldsymbol{\varepsilon}_{o} \boldsymbol{m}_{tunn}^{*}}}$$

ield emission  
$$\rho_{c} = \rho_{1} C_{2} e^{q \phi_{B} / E_{oo}}$$

 $\rho_{c} = \rho_{1} C_{1} e^{q\phi_{B}/E_{o}}$ 





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### **Specific Contact Resistivity**

#### • $\rho_c$ is very sensitive to doping density





### **Contact Resistance**

- **Contacts are characterized by:** 
  - Contact resistance,  $R_c$  ( $\Omega$ )

• Specific contact resistivity,  $ho_c$  ( $\Omega$  -cm<sup>2</sup>)





## **Contact - Current Crowding**

- Current flowing into/out of a contact can crowd into a portion of the contact
- Current has the choice to flow through  $\rho_c$  or  $R_{sh}$





### **Contact - Current Crowding**







### **Contact String (Chain)**

- 2N seriesconnected contacts are measured (N ≈ 100-1000 or more)
- Does not allow separation of R<sub>m</sub>, R<sub>s</sub>, and R<sub>c</sub>
- Suitable for process control, but not for detailed contact characterization
- Must know R<sub>s</sub> from independent measure of sheet resistance in order to extract R<sub>c</sub>



$$\mathbf{R} = \mathbf{N}(\mathbf{R}_m + \mathbf{R}_s + 2\mathbf{R}_c) \approx \mathbf{N}(\mathbf{R}_s + 2\mathbf{R}_c)$$



# **Contact String (Chain)**

- Contact string consists of *pn* junctions
- The junctions are reverse biased
- When the junction voltage exceeds V<sub>BD</sub>, it breaks down
- Can have the case where poor contacts, *i.e.*, high R<sub>c</sub>, cannot be detected because junctions break down
- Measurement depends on substrate grounded or not





### **Transfer Length Method (TLM)**

#### Consists of identical contacts with varying spacings d



$$R_T = 2R_c + R_s \approx \frac{2\rho_c}{L_T W} + \frac{R_{sh}d}{W}$$

$$R_T \approx \frac{2R_{sh}(L_T)^2}{L_T W} + \frac{R_{sh}d}{W} = \frac{R_{sh}(d+2L_T)}{W}$$



### Four Terminal (Kelvin) Method

 Determines the contact resistance independent of the sheet resistance



$$R_c = \frac{V_{12}}{I}; \rho_c \approx R_c WL$$

Heed warnings on accuracy (too complex to discuss in class) summarized in our text on pages 150-156.





### **Via Contact Resistance**





### **Review Questions**

- What is the most important parameter to give low contact resistance?
- What are the three metal-semiconductor conduction mechanisms?
- What is Fermi level pinning?
- Does the contact chain give detailed contact characterization? Why or why not?
- What is the transfer length method?
- Why is the Kelvin contact test structure best for contact resistance measurements?