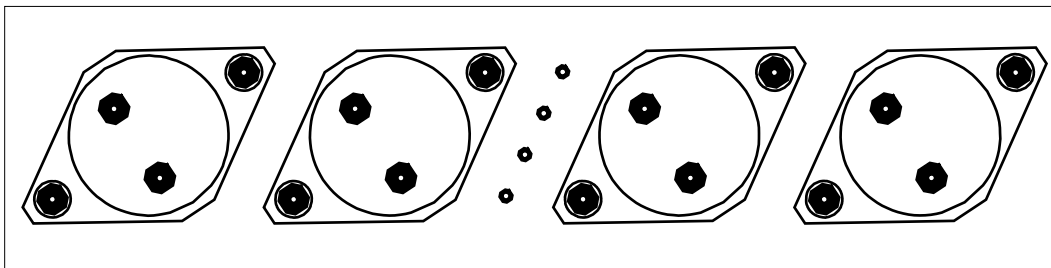


CIRCUIT BOARD LAYOUT (SOLDER SIDE VIEW)



TEMPLATE FOR DRILLING HEAT SINKS FOR
OUTPUT TRANSISTORS AND BIAS DIODES