

Brief Biographical Sketch

Prof. Gabriel A. Rincón-Mora, Ph.D., IEEE Fellow, IET Fellow
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Gabriel A. Rincón-Mora was born in Caracas and raised in Maracay (Venezuela) and migrated to North Miami Beach (Florida) when he was 11 years old. He is now a professor at Georgia Tech in Atlanta (Georgia).

Gabriel Alfonso Rincón-Mora (B.S., M.S., Ph.D., IEEE Fellow, and IET Fellow) was Senior Integrated Circuit (IC) Design Team Leader for Texas Instruments in 1994-2003, Adjunct Professor for Georgia Tech in 1999-2001, and is Visiting Professor at National Cheng Kung University in Taiwan since 2011 and Full Professor with Tenure at Georgia Tech, whose faculty he joined in 2001. His scholarly products include 8 books, 2 book chapters, 37 patents issued, over 140 scientific publications, over 26 commercial power chip designs, and over 70 international speaking engagements. Awards include the Society of Professional Hispanic Engineers' (SHPE) "National Hispanic in Technology Award," Florida International University's "Charles E. Perry Visionary Award," a "Commendation Certificate" from the Lieutenant Governor of California, IEEE CASS Service Award, Robins Air Force Base's "Orgullo Hispano" and "Hispanic Heritage" awards, and induction into Georgia Tech's "Council of Outstanding Young Engineering Alumni" in 2000. *Hispanic Business* magazine also named him one of "The 100 Most Influential Hispanics" in 2000. He received an "Honorable Mention" from the National Science Foundation (NSF) in 1993 and was a Florida Undergraduate Scholar and a Florida International University Faculty Scholar in 1989-1992.

Prof. Rincón-Mora is an IEEE Fellow "for contributions to energy and power integrated circuit design" (2011) and an Institution of Engineering and Technology (IET) Fellow (2009). He has been Chairman of Atlanta's Joint IEEE Solid-State Circuits Society (SSCS) and Circuits and Systems Society (CASS) Chapter since 2004; IEEE Distinguished Lecturer in 2009-2010; Associate Editor for the *IEEE Transactions on Circuits and Systems II* (TCAS II) in 2008-2009 and 2010-2011; Associate Editor for the *IEEE Journal of Solid-State Circuits* in 2011; Editorial Board Member for the *Journal of Low-Power Electronics* in 2009-2011; Guest Editor for TCAS II in 2011; Technical Program Co-Chair for IEEE International SoC Conference in 2011; General Chair for the Semiconductor Research Corporation's Energy and Power IC Workshop in 2009; Guest Co-Editor for *Analog Integrated Circuits and Signal Processing* Journal in 2009; Circuit Design Vice Chair for IEEE International Caribbean Conference on Devices, Circuits and Systems in 2008; Technical Program Chair for IEEE Midwest Symposium of Circuits and Systems (MWSCAS) in 2007; Technical Program Co-Chair for IEEE MWSCAS in 2006; Steering Committee Member for MWSCAS in 2006-2011; Small Business Innovation Research (SBIR) Selection Committee Review Panelist for the National Science Foundation in 2003-2007; and Vice Chairman of Atlanta's SSCS-CASS Chapter in 2004. He served on IEEE CASS Fellows Evaluation Committee in 2011 and IEEE CASS Distinguished Lecturer Committee in 2011-2012. He is Technical Committee Member for IEEE CASS' Analog Signal Processing and Power and Energy Circuits and Systems, a Life Member of the SHPE, a Life Member of Tau Beta Pi, and a member of Eta Kappa Nu and Phi Kappa Phi.

Prof. Rincón-Mora's research is on the design and development of silicon-based integrated circuits (ICs) that draw and condition power from tiny (e.g., lithium-based) batteries, (e.g., direct-methanol) fuel cells, and energy-harnessing (e.g., piezoelectric, electrostatic, photovoltaic, and thermo-electric) generators (which derive energy from motion, light, and temperature) to supply mobile, portable, and self-sustaining devices like wireless microsensors for biomedical, consumer, industrial, and military applications.

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