

CURRICULUM VITA

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I. Earned Degrees

- B.S.** Electrical Engineering, **Florida International University** (GPA 3.83, *Faculty Scholar, FL Undergraduate Scholar, High Honors*), **1992**
M.S. Electrical Engineering (Minor: Mathematics), **Georgia Institute of Technology** (GPA 3.82), **1994**
Ph.D. Electrical Engineering (Minor: Mathematics), **Georgia Institute of Technology** (*Outstanding Ph.D. Graduate*), **1996**

II. Employment

- Electrical Engineer and Laboratory System Specialist*, Fiberworld Conference Center, **Northern Telecom**, **1993**
Analog IC Design Engineer, Standard Linear Group, **Texas Instruments**, **1994 – 1996**
Senior Design Engineer and Design Team Leader, **Texas Instruments**, **1997 – 2001**
Member of Group Technical Staff, **Texas Instruments**, **1999 – 2003**
Adjunct Professor, Electrical and Computer Engineering, **Georgia Institute of Technology**, **1999 – 2001**
Senior Analog IC Design Consultant, **Texas Instruments**, **2003 – 2004**
Assistant Professor, Electrical and Computer Engineering, **Georgia Institute of Technology**, **2001 – 2007**
Director, Georgia Tech Analog Consortium, **Georgia Institute of Technology**, **2001 – 2004**
Associate Professor with Tenure, Electrical and Computer Engineering, **Georgia Institute of Technology**, **2007 – 2012**
Visiting Professor, Electrical Engineering, **National Cheng Kung University**, Taiwan, **since 2011**
Professor, Electrical and Computer Engineering, **Georgia Institute of Technology**, **since 2012**

III. Scholarly Products

Ph.D. Dissertation:

- G.A. Rincón-Mora, *Current Efficient, Low Voltage, Low Dropout Regulators*. Georgia Institute of Technology, 1996
(Advisor: Prof. Phil Allen).

Books:

- B1. G.A. Rincón-Mora, *Voltage References*. New Jersey: IEEE Press and John Wiley & Sons, Inc. (192 pgs), **2001**.
B2. G.A. Rincón-Mora, *Power Management ICs*. Raleigh: Lulu (268 pgs), **2005**.
B3. G.A. Rincón-Mora, *Analog IC Design with Low-Dropout Regulators*. New York: McGraw-Hill (400 pgs), Jan. **2009**.
B4. G.A. Rincón-Mora, *Analog IC Design*. Raleigh: Lulu (238 pgs), **2009**.
B5. G.A. Rincón-Mora, *Power IC Design*. Raleigh: Lulu (264 pgs), **2009**.
B6. G.A. Rincón-Mora, *Short Stories and Poems to Boot!* New York: Vantage Press (86 pgs), **2001** [Short Stories/Poetry].
B7. G.A. Rincón-Mora, *Triple Engagement*. New York: iUniverse (160 pgs), **2004** [Short Stories/Poetry].
B8. G.A. Rincón-Mora, *Vanish*. Raleigh: Lulu (148 pgs), **2009** [Novella].

Book Chapters:

- BC1. G.A. Rincón-Mora, "**Harvesting Microelectronic Circuits**," *Energy Harvesting Technologies* (Editors: S. Priya and D.J. Inman), Springer, Jan. **2009**.
BC2. G.A. Rincón-Mora, "**Energizing and Powering Microsystems**," *Integrated Microsystems: Electronics, Photonics, and Biotechnology* (Editor: K. Iniewski), CRC Press, Oct. **2011**.

Patents Issued: * Boldface inventors are(were) students Prof. Rincón advises(d).

- P1. G.A. Rincón *et al.*, "Amplifier Circuit and Method," **U.S. 5,491,437**, Feb. 13, **1996**.
P2. G.A. Rincón *et al.*, "Controlled Current Output Stage Amplifier Circuit and Method," **U.S. 5,500,625**, Mar. 19, **1996**.
P3. G.A. Rincón and M. Corsi, "Cross Coupled Quad Comparator for Current Sensing Independent of Temperature," **U.S. 5,519,341**, May 21, **1996**.
P4. G.A. Rincón *et al.*, "Amplifier Circuit and Method," **EP 0,715,405**, Jun. 5, **1996**.
P5. G.A. Rincón *et al.*, "Amplifier Circuit and Method," **JP 8,237,046**, Sept. 13, **1996**.
P6. G.A. Rincón and M. Corsi, "Current Sensing Circuit and Method," **U.S. 5,614,850**, Mar. 25, **1997**.

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- P7. G.A. Rincón *et al.*, "A Voltage Regulator," **EP 0,851,332**, Jan. 7, **1998**.
- P8. G.A. Rincón *et al.*, "Drop-out Voltage Controller," **JP 10,187,258**, Jul. 14, **1998**.
- P9. G.A. Rincón-Mora *et al.*, "Low Drop-Out Regulator with PMOS Pass Element," **U.S. 5,867,015**, Feb. 2, **1999**.
- P10. G.A. Rincón-Mora, "Voltage Loss Compensation for DC-DC Converters," **EP 0,928,056**, Jul. 7, **1999**.
- P11. G.A. Rincón-Mora, "Low Voltage, Current-Mode, Piecewise-Linear Curvature Corrected Bandgap Reference," **U.S. 5,952,873**, Sept. 14, **1999**.
- P12. G.A. Rincón-Mora, "Optimized Frequency Shaping Circuit Topologies for LDOs," **U.S. 5,982,226**, Nov. 9, **1999**.
- P13. G.A. Rincón-Mora and M. Corsi, "Current-efficient low-drop-out voltage regulator with improved load regulation and frequency response," **EP 0,957,421**, Nov. 17, **1999**.
- P14. G.A. Rincón-Mora *et al.*, "Low-Dropout Voltage Regulator Incorporating a Current Efficient Transient Response Boost Circuit," **U.S. 6,046,577**, Apr. 4, **2000**.
- P15. G.A. Rincón-Mora, "Increase in Active Compensation Capacitive Property," **JP 2000,151,296**, May 30, **2000**.
- P16. G.A. Rincón-Mora, "Active Compensating Capacitive Multiplier," **EP 1,006,648**, Jun. 7, **2000**.
- P17. G.A. Rincón-Mora, "Active Compensating Capacitive Multiplier," **U.S. 6,084,475**, Jul. 4, **2000**.
- P18. G.A. Rincón-Mora, "An Exact Curvature-Correcting Method for Bandgap Circuits," **U.S. 6,157,245**, Dec. 5, **2000**.
- P19. G.A. Rincón-Mora, "Bandgap circuits with curvature-correction," **EP 1,041,480**, Oct. 4, **2000**.
- P20. G.A. Rincón-Mora and M. Corsi, "Current-efficient low-drop-out voltage regulator with improved load regulation and frequency response," **U.S. 6,188,211**, Feb. 13, **2001**.
- P21. G.A. Rincón-Mora, "Accurate, Fast, and User Programmable Hysteretic Comparator," **U.S. 6,229,350**, May 8, **2001**.
- P22. G.A. Rincón-Mora and **M. Huggins**, "High Power Supply Ripple Rejection Internally Compensated Low Drop-Out Voltage Regulator Using PMOS Pass Device," **U.S. 6,304,131**, Oct. 16, **2001**.
- P23. G.A. Rincón-Mora, "Integrated Low Ripple, High Frequency Hysteretic Controller for dc-dc Converters," **U.S. 6,369,555**, Apr. 9, **2002**.
- P24. G.A. Rincón-Mora and **B. Abesingha**, "Method of Minimizing Package-Shift Effects in Integrated Circuits by Using a Thick Metallic Overcoat," **U.S. 6,432,753**, Aug. 13, **2002**.
- P25. G.A. Rincón-Mora, "Adjustable Temperature-Compensated Threshold Circuit with Trip-Points Exceeding the Given Supplies," **EP 1,265,363**, Nov. 12, **2002**.
- P26. G.A. Rincón-Mora, "Adjustable Temperature Compensated Threshold Circuit," **JP 2002,368,587**, Dec. 20, **2002**.
- P27. G.A. Rincón-Mora and **R. Stair**, "Buffer/Driver for Low Dropout Regulators," **U.S. 6,501,305**, Dec. 31, **2002**.
- P28. G.A. Rincón-Mora, "Adjustable Temperature-Compensated Threshold Circuit with Trip-Points Exceeding the Given Supplies," **U.S. 6,545,511**, Apr. 8, **2003**.
- P29. G.A. Rincón-Mora and **M. Pulkin**, "Stable Low Dropout, Low Impedance Driver for Linear Regulators," **U.S. 6,573,694**, Jun. 3, **2003**.
- P30. G.A. Rincón-Mora, "Temperature-Compensated Threshold Circuit," **EP 1,351,063**, Aug. 10, **2003**.
- P31. G.A. Rincón-Mora, "Integrated low ripple, high frequency power efficient hysteretic controller for dc-dc converters," **U.S. 6,628,109**, Sept. 30, **2003**.
- P32. G.A. Rincón-Mora *et al.*, "Semiconductor device which minimizes package-shift effects in integrated circuits by using a thick metallic overcoat," **U.S. 6,750,553**, Jun. 15, **2004**.
- P33. G.A. Rincón-Mora and **R. Stair**, "Circuit and method to facilitate threshold voltage extraction and facilitate operation of a capacitor multiplier," **U.S. 6,806,762**, Oct. 19, **2004**.
- P34. G.A. Rincón *et al.*, "A Voltage Regulator," **DE 69,727,783**, Dec. 30, **2004**.
- P35. G.A. Rincón-Mora, **V. Gupta**, and P. Raha, "Low Dropout Monolithic Linear Regulator Having Wide Operating Load Range," **U.S. 6,847,260**, Jan. 25, **2005**.
- P36. G.A. Rincón-Mora and **M. Arnold**, "Voltage Regulator with Low Dropout Voltage (Mode-Hopping Buffer with Rail-to-Rail Output for Low Dropout)," **U.S. 7,339,416**, Mar. 4, **2008**.
- P37. G.A. Rincón-Mora and **M. Arnold**, "Gate driver circuit for power transistor," **U.S. 7,560,973**, Jul. 14, **2009**.
- [Pending] **D. Kwon** and G.A. Rincón-Mora, "Rectifier-free Piezoelectric Energy Harvester and Battery Charger," **GTID 5206** [provisional filed in 2010 and actual application filed in **2011**].

Journal Articles: * Boldface authors are(were) students/engineers Prof. Rincón advises(d).

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- J1. G.A. Rincón-Mora and P.E. Allen, "A Low-Voltage, Low Quiescent Current, Low Drop-Out Regulator," *IEEE Journal of Solid-State Circuits (JSSC)*, vol. 33, no. 1, pp. 36-44, Jan. 1998.
- J2. G.A. Rincón-Mora and P.E. Allen, "Optimized Frequency-Shaping Circuit Topologies for LDO's," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 45, no. 6, pp. 703-708, Jun. 1998.
- J3. B.J. Blalock, P.E. Allen, and G.A. Rincón-Mora, "Designing 1V Op Amps Using Standard Digital CMOS Technology," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 45, no. 7, pp. 769-780, Jul. 1998.
- J4. G.A. Rincón-Mora and P.E. Allen, "A 1.1 V Current-Mode and Piecewise-Linear Curvature Corrected Bandgap Reference," *IEEE Journal of Solid-State Circuits (JSSC)*, vol. 33, no. 10, pp. 1551-1554, Oct. 1998.
- J5. G.A. Rincón-Mora, "Active Capacitor Multiplier in Miller-Compensated Circuits," *IEEE Journal of Solid-State Circuits (JSSC)*, vol. 35, no. 1, pp. 26-32, Jan. 2000.
- J6. **R. Stair** and G.A. Rincón-Mora, "A Low Voltage, Rail-to-Rail, Class AB CMOS Amplifier With High Drive and Low Output Impedance Characteristics," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 48, no. 8, pp. 753-761, Aug. 2001.
- J7. **B. Abesingha**, G.A. Rincón-Mora, and D. Briggs, "Voltage Shift in Plastic-Packaged Bandgap References," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 49, no. 10, pp. 681-685, Oct. 2002.
- J8. **R. Dokania** and G.A. Rincón-Mora, "Cancellation of Load-Regulation in Low Drop-Out Regulators," *IET Electronic Letters (IET-EL)*, vol. 38, issue 22, pp. 1300-1302, Oct. 2002.
- J9. **B. Sahu** and G.A. Rincón-Mora, "A High-Efficiency Linear RF Power Amplifier with a Power-Tracking Dynamically Adaptive Buck-Boost Supply," *IEEE Transactions on Microwave Theory and Techniques (TMTT)*, vol. 52, no. 1, pp. 112-120, Jan. 2004.
- J10. **B. Sahu** and G.A. Rincón-Mora, "A Low Voltage, Non-Inverting, Dynamic, Synchronous Buck-Boost Converter for Portable Applications," *IEEE Transactions on Power Electronics (TPE)*, vol. 19, no. 2, pp. 443-452, Feb. 2004.
- J11. **S. Zhou** and G.A. Rincón-Mora, "A High Efficiency, Soft Switching DC-DC Converter with Adaptive Current-Ripple Control for Portable Applications," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 53, no. 4, pp. 294-298, Apr. 2006.
- J12. **M. Chen** and G.A. Rincón-Mora, "Accurate Electrical Battery Model Capable of Predicting Runtime and I-V Performance," *IEEE Transactions on Energy Conversion (TEC)*, vol. 21, no. 2, pp. 504-511, Jun. 2006.
- J13. **M. Chen** and G.A. Rincón-Mora, "Accurate, Compact, and Power Efficient Li-Ion Battery Charger Circuit," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 53, no. 11, pp. 1180-1184, Nov. 2006.
- J14. **H.P. Forghani-zadeh** and G.A. Rincón-Mora, "Low-Power CMOS Ramp Generator Circuit for DC-DC Converters," *Journal of Low Power Electronics (JOLPE)*, vol. 2, no. 3, pp. 437-441, Dec. 2006.
- J15. **B. Sahu** and G. A. Rincón-Mora, "An Accurate, Low Voltage, CMOS Switching Power Supply with Adaptive On-Time Pulse-Frequency Modulation," *IEEE Transactions on Circuits and Systems I (TCAS I)*, vol. 54, no. 2, pp. 312-321, Feb. 2007.
- J16. **B. Sahu** and G.A. Rincón-Mora, "A High Efficiency WCDMA RF Power Amplifier (PA) with Adaptive, Dual-Mode Buck-Boost Supply and Bias-Current Control," *IEEE Microwave and Wireless Components Letters (MWCL)*, vol. 17, no. 3, pp. 238-240, Mar. 2007.
- J17. **V. Gupta** and G.A. Rincón-Mora, "Achieving Less Than 2% 3- σ Mismatch with Minimum Channel-Length CMOS Devices," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 54, no. 3, pp. 232-236, Mar. 2007.
- J18. **H.P. Forghani-zadeh** and G.A. Rincón-Mora, "An Accurate, Continuous, and Lossless Self-Learning CMOS Current-Sensing Scheme for Inductor-Based DC-DC Converters," *IEEE Journal of Solid-State Circuits (JSSC)*, vol. 42, no. 3, pp. 665-679, Mar. 2007.
- J19. **H.P. Forghani-zadeh** and G.A. Rincón-Mora, "A strategy for fast and reliable top-level simulation and verification of mixed-signal DC-DC converter ICs," *IEE Proceedings on Circuits, Systems, and Devices (IET-CSD)*, vol. 1, no. 2, pp. 143-150, Apr. 2007.
- J20. **H.P. Forghani-zadeh** and G.A. Rincón-Mora, "A Programmable 210 μ V Offset Rail-to-Rail G_m -C filter," *IEEE Transactions on Circuits and Systems I (TCAS I)*, vol. 54, no. 8, pp. 1636-1646, Aug. 2007.
- J21. **V. Gupta** and G.A. Rincón-Mora, "Low Output Impedance 0.6 μ m-CMOS Sub-Bandgap Reference," *IET Electronic Letters (IET-EL)*, vol. 43, pp. 1085-1087, Sept. 2007.
- J22. **N. Keskar** and G.A. Rincón-Mora, "A Fast, Sigma-Delta Boost DC-DC Converter Tolerant to Wide LC Filter Variations," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 55, pp. 198-202, Feb. 2008.

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- J23. **N. Keskar** and G.A. Rincón-Mora, "A Compact 1-30 μ H, 1-350 μ F, 5-50m Ω ESR Compliant, 1.5% Accurate 0.6 μ m CMOS Differential Sigma-Delta Boost DC-DC Converter," *Analog Integrated Circuits and Signal Processing Journal (AICSP)*, vol. 54, no. 3, pp. 157-169, **2008**.
- J24. **M. Chen** and G.A. Rincón-Mora, "A Compact Electrical Model for Microscale Fuel Cells Capable of Predicting Runtime and I-V Polarization Performance," *IEEE Transactions on Energy Conversion (TEC)*, vol. 23, no. 3, pp. 842-850, Sept. **2008**.
- J25. **E.O. Torres** and G.A. Rincón-Mora, "Energy-harvesting system-in-package (SiP) microsystem," *ASCE Journal of Energy Engineering (JEE)*, **Invited**, vol. 134, no. 4, pp. 121-129, Dec. **2008**.
- J26. **S. Kim** and G.A. Rincón-Mora, "Achieving High Efficiency under Micro-Watt Loads with Switching Buck DC-DC Converters," *Journal of Low Power Electronics (JOLPE)*, vol. 5, no. 2, pp. 229-240, Aug. **2009**.
- J27. **D. Kwon** and G.A. Rincón-Mora, "Single-Inductor Multiple-Output (SIMO) Switching DC-DC Converters," *IEEE Transactions on Circuits and Systems II (TCAS II)*, **Invited**, vol. 56, no. 8, Aug. **2009**.
- J28. **E.O. Torres** and G.A. Rincón-Mora, "Electrostatic Energy-Harvesting and Battery-Charging CMOS System Prototype," *IEEE Transactions on Circuits and Systems I (TCAS I)*, vol. 56, no. 9, pp. 1938-1948, Sept. **2009**.
- J29. **L.A. Milner** and G.A. Rincón-Mora, "Limits of Predictive Current-Ripple Suppression in Switching Power Supply ICs," *IET Power Electronics (IET-PE)*, vol. 3, no. 1, pp. 43-53, Jan. **2010**.
- J30. **V. Gupta** and G.A. Rincón-Mora, "A Low-Impedance, Sub-Bandgap 0.6 μ m CMOS Reference with 0.84% Trimless 3-sigma Accuracy and -30dB Worst-Case PSRR up to 50MHz," *Analog Integrated Circuits and Signal Processing Journal (AICSP)*, vol. 62, no. 3, p. 345, **2010**.
- J31. **E.O. Torres** and G.A. Rincón-Mora, "A 0.7 μ m BiCMOS Electrostatic Energy-Harvesting System IC," *IEEE Journal of Solid-State Circuits (JSSC)*, vol. 45, no. 2, pp. 483-496, Feb. **2010**.
- J32. **N. Keskar** and G.A. Rincón-Mora, "One Clock-Cycle Response 0.5 μ m CMOS Dual-Mode Sigma-Delta DC-DC Bypass Boost Converter Stable over Wide R_{ESR}LC Variations," *Advances in Power Electronics (APE)*, vol. 2010, no. 253508, p. 9, **2010**.
- J33. **L.A. Milner** and G.A. Rincón-Mora, "A Feed-Forward 10x CMOS Current-Ripple Suppressor for Switching Power Supplies," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 57, no. 5, pp. 354-378, May **2010**.
- J34. **E.O. Torres** and G.A. Rincón-Mora, "Self-Tuning Electrostatic Energy-Harvester IC," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 57, no. 10, pp. 808-812, Oct. **2010**.
- J35. **A. Patel** and G.A. Rincón-Mora, "High Power-Supply-Rejection (PSR) Current-Mode Low-Dropout (LDO) Regulator," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 57, no. 11, pp. 868-873, Nov. **2010**.
- J36. **D. Kwon** and G.A. Rincón-Mora, "A 2- μ m BiCMOS Rectifier-free AC-DC Piezoelectric Energy Harvester-Charger IC," *IEEE Transactions on Biomedical Circuits and Systems (TBioCAS)*, **Invited**, vol. 4, no. 6, pp. 400-409, Dec. **2010**.
- J37. **D. Kwon**, G.A. Rincón-Mora, and **E.O. Torres**, "Harvesting Ambient Kinetic Energy with Switched-Inductor Converters" *IEEE Transactions on Circuits and Systems I (TCAS I)*, **Invited**, vol. 58, no. 7, pp. 1551-1560, July **2011**.
- J38. **R.D. Prabha**, **D. Kwon**, **O. Lazaro**, **K.D. Peterson**, and G.A. Rincón-Mora, "Increasing Electrical Damping in Energy-harnessing Transducers," *IEEE Transactions on Circuits and Systems II (TCAS II)*, Special Issue on Energy Harvesting, vol. 58, no. 12, pp. 787-791, Dec. **2011**.
- J39. **L.A. Milner** and G.A. Rincón-Mora, "Small Saturating Inductors for More Compact Switching Power Supplies," *IEEE Transactions on Electrical and Electronic Engineering (TEEE)*, vol. 7, no. 1, pp. 69-73, Jan. **2012**.
- J40. **S. Kim** and G.A. Rincón-Mora, "Single-Inductor Fuel Cell-Li Ion Charger-Supply IC with Nested Hysteretic Control," *Analog Integrated Circuits and Signal Processing Journal (AICSP)*, vol. 70, no. 1, Page 33-45, Jan. **2012**.
- J41. G.A. Rincón-Mora, **A.A. Blanco**, and **J. P. Vogt**, "A 1.3-W, 0.6-m CMOS Current-Frequency Analog-Digital Converter for Implantable Blood-Glucose Monitors," *Journal of Low Power Electronics (JOLPE)*, vol. 8, pp. 47-57, Feb. **2012**.
- J42. **O. Lazaro** and G.A. Rincón-Mora, "1 - 50-MHz VHF Electromagnetic Sensor-interface Power-attenuation Detector Circuit," *International Journal of Electronics and Communications (IJEC)* [Accepted: November **2011**].

Conference Articles: * Boldface authors are(were) students/engineers Prof. Rincón advises(d).

- C1. P.E. Allen, B.J. Blalock, and G.A. Rincón, "A 1V CMOS Op Amp Using Bulk-Driven MOSFETs," *IEEE International Solid-State Circuits Conference (ISSCC)*, Session 11, pp. 192-193, San Francisco, California, **1995**.
- C2. P.E. Allen, B.J. Blalock, and G.A. Rincón, "Low Voltage Analog Circuits Using Standard CMOS Technology," *IEEE International Symposium on Low Power Design (ISLPD)*, pp. 209-214, Laguna, California, **1995**.
- C3. **B. Sahu** and G.A. Rincón-Mora, "System-Level Requirements of DC-DC Converters for Dynamic Power Supplies of Power Amplifiers," *IEEE Asia-Pacific Conference on ASICs (AP-ASIC)*, pp. 149-152, Taipei, Taiwan, **2002**.

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- C4. **M. Gildersleeve, H.P. Forghani-zadeh**, and G.A. Rincón-Mora, "A Comprehensive Power Analysis and a Highly Efficient, Mode-Hopping DC-DC Converter," IEEE Asia-Pacific Conference on ASICs (**AP-ASIC**), pp. 153-156, Taipei, Taiwan, **2002**.
- C5. **V. Gupta** and G.A. Rincón-Mora, "Predicting the Effects of Error Sources in Bandgap Reference Circuits and Evaluating their Design Implications," IEEE Midwest Symposium on Circuits and Systems (**MWSCAS**), vol. 3, pp. 575-578, Tulsa, Oklahoma, **2002**.
- C6. **H.P. Forghani-zadeh** and G.A. Rincón-Mora, "Current-Sensing Techniques for DC-DC Converters," IEEE Midwest Symposium on Circuits and Systems (**MWSCAS**), vol. 2, pp. 577-580, Tulsa, Oklahoma, **2002**.
- C7. **A. Makharria** and G.A. Rincón-Mora, "Integrating Power Inductors onto the IC - SOC Implementation of Inductor Multipliers for DC-DC Converters," IEEE Industrial Electronics Conference (**IECON**), vol. 1, pp. 556-561, Roanoke, Virginia, **2003**.
- C8. **V. Gupta**, G. A. Rincón-Mora, and P. Raha, "Analysis and Design of Monolithic, High PSR, Linear Regulators for SoC Applications," IEEE International System on Chip Conference (**ISOCC**), pp. 311-315, Santa Clara, California, **2004**.
- C9. **N.A. Keskar** and G.A. Rincón-Mora, "Self-Stabilizing, Integrated, Hysteretic Boost DC-DC Converter," IEEE Industrial Electronics Conference (**IECON**), TA3-4, vol. 1, pp. 586-591, Busan, Korea, Nov. 2 - 6, **2004**.
- C10. **M. Chen** and G.A. Rincón-Mora, "A Self-Powered, Self-Sustaining System-on-Chip (SOC) Solution Powered from Hybrid Micro-Fuel Cells," **Army Science Conference**, Orlando, Florida, Nov. 28 - Dec. 2, **2004**.
- C11. **B. Sahu** and G.A. Rincón-Mora, "Adaptive Power Management of Linear RF Power Amplifiers- An Integrated System Design Approach," IEEE Asia-Pacific Microwave Conference (**APMC**), New Delhi, India, Dec. 15 - 18, **2004**.
- C12. **B. Sahu** and G.A. Rincón-Mora, "A high-efficiency, dual-mode, dynamic, buck-boost power supply IC for portable applications," IEEE International Conference on VLSI Design (**IC-VLSI**), pp. 858-861, Kolkata, India, Jan. **2005**.
- C13. **V. Gupta** and G.A. Rincón-Mora, "Predicting and Designing for the Impact of Process Variations and Mismatch on the Trim Range and Yield of Bandgap References," IEEE International Symposium on Quality Electronic Design (**ISQED**), pp. 503-508, Santa Clara, California, **2005**.
- C14. **V. Gupta** and G.A. Rincón-Mora, "A Low Dropout, CMOS Regulator with High PSR over Wideband Frequencies," IEEE International Symposium on Circuits and Systems (**ISCAS**), vol. 5, pp. 4245-4248, Tokyo, Japan, May, **2005**.
- C15. **E.O. Torres** and G.A. Rincón-Mora, "Long Lasting, Self-Sustaining, and Energy-Harvesting System-in-Package (SiP) Sensor Solution," International Conference on Energy, Environment, and Disasters (**INCEED**), Session A-2, ID 368, pp. 1-33, Charlotte, NC, Jul. **2005**.
- C16. **L.A. Milner** and G.A. Rincón-Mora, "A Novel Predictive Inductor Multiplier for Integrated Circuit DC-DC Converters in Portable Applications," International Symposium on Low Power Electronics and Design (**ISLPED**), pp. 84-89, San Diego, CA, U.S.A., Aug. **2005**.
- C17. **H.P. Forghani-zadeh** and G.A. Rincón-Mora, "A Low Glitch, Continuous Low-Offset, Programmable Gain and Bandwidth, Gm-C Filter," IEEE Midwest Symposium on Circuits and Systems (**MWSCAS**), pp. 1629-1632, Cincinnati, Ohio, Aug. 7 - 10, **2005**.
- C18. **H.P. Forghani-zadeh** and G.A. Rincón-Mora, "A Lossless, Accurate, Self Calibrating Current-Sensing Technique for DC-DC Converters," IEEE Industrial Electronics Conference (**IECON**), PE-03, pp. 549-554, Raleigh, North Carolina, Nov. 6 - 10, **2005**.
- C19. **N. Keskar** and G.A. Rincón-Mora, "A High Bandwidth, Bypass, Transient-Mode Sigma-Delta DC-DC Switching Boost Regulator with Wide LC Compliance," IEEE Industrial Electronics Conference (**IECON**), PE-03, pp. 543-548, Raleigh, North Carolina, Nov. 6 - 10, **2005**.
- C20. **E. Torres, L. Milner, N. Keskar, M. Chen, H. Pan, V. Gupta, P. Forghani**, and G.A. Rincón-Mora, "SiP Integration of Intelligent, Adaptive, Self-Sustaining Power Management Solutions for Portable Applications," IEEE International Symposium on Circuits and Systems (**ISCAS**), pp. 5311-5314, Kos, Greece, May 21 - 24, **2006**.
- C21. **H.I. Pan** and G.A. Rincón-Mora, "Asynchronous Nonlinear Power-Tracking Supply for Power Efficient Linear RF PAs," IEEE International Conference on Communications, Circuits, and Systems (**ICCCAS**), pp. 2531-2535, Guilin, China, Jun. 25 - 28, **2006**.
- C22. **E.O. Torres** and G.A. Rincón-Mora, "Electrostatic energy harvester and Li-Ion charger for micro-scale applications," IEEE Midwest Symposium on Circuits and Systems (**MWSCAS**), pp. 65-69, San Juan, Puerto Rico, Aug. 6 - 9, **2006**.
- C23. **V. Gupta** and G.A. Rincón-Mora, "A 5mA 0.6 μ m CMOS Miller-Compensated LDO Regulator with -27dB Worst Case Power Supply Rejection Using 60pF of On-Chip Capacitance," IEEE International Solid-State Circuits Conference (**ISSCC**), pp. 520-521, San Francisco, CA, Feb. **2007**.

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- C24. **J.P. Vogt** and G.A. Rincón-Mora, "SiP Wireless Micro-Power Sensors," **Government** Microcircuit Applications and Critical Technology Conference, **Invited**, Lake Buena Vista, FL, Mar. 21, **2007**.
- C25. **N. Keskar** and G.A. Rincón-Mora, "Designing an Accurate and Robust LC-Compliant Asynchronous Sigma-Delta Boost DC-DC Converter," IEEE International Symposium on Circuits and Systems (**ISCAS**), pp. 549-552, New Orleans, USA, May 27 - 30, **2007**.
- C26. **M. Chen, J.P. Vogt**, and G.A. Rincón-Mora, "Design Methodology of a Hybrid Micro-Scale Fuel Cell-Thin-Film Lithium Ion Source," IEEE International Midwest Symposium on Circuits and Systems (**MWSCAS**), pp. 674-677, Montreal, Canada, Aug. 5 - 8, **2007**.
- C27. **M. Chen** and G.A. Rincón-Mora, "Single Inductor, Multiple Input, Multiple Output (SIMIMO) Power Mixer-Charger-Supply System," International Symposium on Low Power Electronics and Design (**ISLPED**), pp. 310-315, Portland, Oregon, USA, Aug. 27 - 29, **2007**.
- C28. W. Mustain, S. Prakash, H. Kim, P. Kohl and G. Rincón-Mora, "Micro DMFC - Lithium Ion Hybrid Power Source for Low Power Applications," **Meeting of the Electrochemical Society**, Washington, DC, Oct. 7 - 12, **2007**.
- C29. **S. Kim**, G.A. Rincón-Mora, S. Kim, P. Kohl, **O. Lazaro**, W. Mustain, S. Prakash, D. Rivera, J. Vogt, and F. Sienkiewicz, "1cm³ Fuel-Cell, Li-Ion Powered, Wireless Sensor Instrumentation Chip," **ITEA Test Instrumentation Workshop, Invited**, Lancaster, CA, May 5 - 9, **2008**.
- C30. **L.A. Milner** and G.A. Rincón-Mora, "Mixing-Sourcing Technologies to Extend the Operational Lifetime of Ultra-Portable Micro-Scale Electronics," **Annual Meeting of the International Society of Electrochemistry, Invited**, Seville, Spain, Sep. **2008**.
- C31. **S. Kim** and G.A. Rincón-Mora, "Single-Inductor Dual-Input Dual-Output Buck-Boost Fuel Cell-Li Ion Charging DC-DC Converter," IEEE International Solid-State Circuits Conference (**ISSCC**), pp. 444-445, San Francisco, CA, Feb. **2009**.
- C32. **O. Lazaro**, G.A. Rincón-Mora, **J. Vogt**, "1 – 50-MHz VHF EMI Instrumentation Sensor Circuit," **ITEA Test Instrumentation Workshop, Invited**, Ridgecrest, California, May 12-14, **2009**.
- C33. **D. Kwon** and G.A. Rincón-Mora, "A Rectifier-Free Piezoelectric Energy Harvester Circuit," IEEE International Symposium on Circuits and Systems (**ISCAS**), pp. 1085-1088, Taipei, Taiwan, May 24 - 27, **2009**.
- C34. **E. Torres** and G.A. Rincón-Mora, "Energy Budget and High-Gain Strategies for Voltage-Constrained Electrostatic Harvesters," IEEE International Symposium on Circuits and Systems (**ISCAS**), pp. 1101-1104, Taipei, Taiwan, May 24 - 27, **2009**.
- C35. **D. Kwon** and G.A. Rincón-Mora, "Operation-Based Signal-Flow AC Analysis of Switching DC-DC Converters in CCM and DCM," IEEE International Midwest Symposium on Circuits and Systems (**MWSCAS**), pp. 957-960, Cancún, Mexico, Aug. 2-5, pp. 957-960, **2009**.
- C36. **D. Kwon** and G.A. Rincón-Mora, "A Single-Inductor AC-DC Piezoelectric Energy-Harvester/Battery-Charger IC Converting $\pm(0.35$ to $1.2V)$ to $(2.7$ to $4.5V)$," IEEE International Solid-State Circuits Conference (**ISSCC**), pp. 494-495, San Francisco, CA, Feb. **2010**.
- C37. **D. Kwon**, G.A. Rincón-Mora, and **E. Torres**, "Harvesting Kinetic Energy with Switched-Inductor DC-DC Converters," IEEE International Symposium on Circuits and Systems (**ISCAS**), **Invited** (Special Session), Paris, France, May 30 - Jun. 2, **2010**.
- C38. **O. Lazaro** and G.A. Rincón-Mora, "Comparative Efficiency Analysis of Dynamically Supplied Power Amplifiers (PA)," IEEE International Conference on Electronics, Circuits, and Systems (**ICECS**), Athens, Greece, Dec. 12 - 15, **2010**.
- C39. **R.D. Prabha**, G.A. Rincón-Mora, and **S. Kim**, "Harvesting Circuits for Miniaturized Photovoltaic Cells," IEEE International Symposium on Circuits and Systems (**ISCAS**), **Invited** (Special Session), pp. 309-312, Rio de Janeiro, Brazil, May 15 - 18, **2011**.
- C40. **O. Lazaro** and G.A. Rincón-Mora, "Minimizing MOSFET Power Losses in Near-field Electromagnetic Energy-harnessing ICs," IEEE International Systems-on-Chip Design Conference (**ISOCC**), Jeju, Korea, Nov. 17 - 18, **2011**.
- C41. **S. Kim**, G.A. Rincón-Mora, and **D. Kwon**, "Extracting the frequency response of switching DC-DC converters in CCM and DCM from time-domain simulations," IEEE International Systems-on-Chip Design Conference (**ISOCC**), Jeju, Korea, Nov. 17 - 18, **2011**.
- C42. **K. Peterson** and G.A. Rincón-Mora, "High-Damping Energy-Harvesting Electrostatic CMOS Charger," IEEE International Symposium on Circuits and Systems (**ISCAS**), Seoul, Korea, May 20 - 23, **2012**.
- C43. **S. Kim** and G.A. Rincón-Mora, "Efficiency of Switched-Inductor DC-DC Converter ICs Across Process Technologies," IEEE International Symposium on Circuits and Systems (**ISCAS**), Seoul, Korea, May 20 - 23, **2012**.

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Trade Articles: * Boldface authors are(were) students/engineers Prof. Rincón advises(d).

- T1. **N. Keskar** and G.A. Rincón-Mora, "A user-friendly boost DC-DC converter topology - it's fast and widely stable," *Power Management Design Line (PMDL)*, Jan. 23, **2005**.
- T2. **N. Keskar** and G.A. Rincón-Mora, "A user-friendly boost DC-DC converter topology - it's fast and widely stable," *Planet Analog*, Jan. 26, **2005**.
- T3. **V. Gupta** and G.A. Rincón-Mora, "Inside the Belly of the Beast: A Map for the Wary Bandgap Reference Designer when Confronting Process Variations," *Power Management Design Line (PMDL)*, Feb. 18, **2005**.
- T4. G.A. Rincón-Mora and **P. Forghani**, "Accurate and Lossless Current-Sensing Techniques: A Practical Myth?" *Power Management Design Line (PMDL)*, Mar. 17, **2005**.
- T5. G.A. Rincón-Mora and **M. Chen**, "Self-powered chips – The work of fiction," *Power Management Design Line (PMDL)*, Apr. 28, **2005**.
- T6. G.A. Rincón-Mora and **M. Chen**, "Self-powered chips – The work of fiction," *Planet Analog*, Apr. 28, **2005**.
- T7. **L. Milner** and G.A. Rincón-Mora, "Taming Power Inductors for System-on-Chip (SoC) Integration," *Power Management Design Line (PMDL)*, May 18, **2005**.
- T8. **N. Keskar** and G.A. Rincón-Mora, "A user-friendly boost DC-DC converter topology," *Electronic Engineering Times Japan (EET Japan – in Japanese)*, no. 1, **2005**.
- T9. **E. Torres** and G.A. Rincón-Mora, "Energy-harvesting chips and the quest for everlasting life," *Power Management Design Line (PMDL)*, Jun. 30, **2005**.
- T10. G.A. Rincón-Mora and **H. Pan**, "Quenching the thirst of RF power amps and extending the life of portable devices," *Power Management Design Line (PMDL)*, Jul. 15, **2005**.
- T11. G.A. Rincón-Mora and **H. Pan**, "Quenching the thirst of RF power amps and extending the life of portable devices," *Planet Analog*, Jul. 31, **2005**.
- T12. **N. Keskar** and G.A. Rincón-Mora, "A fast, accurate, LC compliant DC-DC boost regulator...Is it possible?" *Power Management Design Line (PMDL)*, Aug. 22, **2005**.
- T13. **E. Torres** and G.A. Rincón-Mora, "Harvesting ambient energy will make embedded devices autonomous," *Electronic Engineering Times' Embedded*, Aug. 29, **2005**.
- T14. G.A. Rincón-Mora and **V. Gupta**, "Power Supply Ripple Rejection and Linear Regulators: What's all the noise about?" *Power Management Design Line (PMDL)*, Sept. 20, **2005**.
- T15. **E. Torres** and G.A. Rincón-Mora, "Harvesting ambient energy," *Electronic Engineering Times (EET)*, Aug. 29, **2005**.
- T16. G.A. Rincón-Mora and **V. Gupta**, "Power Supply Ripple Rejection and Linear Regulators: What's all the noise about?" *Planet Analog*, Sept. 20, **2005**.
- T17. G.A. Rincón-Mora and **P. Forghani**, "Self-learning switching DC-DC converters meet smart power," *Power Management Design Line (PMDL)*, Oct. 13, **2005**.
- T18. G.A. Rincón-Mora and **M. Chen**, "Attempting clairvoyance with battery performance," *Power Management Design Line (PMDL)*, Nov. 20, **2005**.
- T19. G.A. Rincón-Mora and **L.A. Milner**, "How to fully integrate switching DC-DC supplies with inductor multipliers," *Power Management Design Line (PMDL)*, Dec. 18, **2005**.
- T20. G.A. Rincón-Mora and **L.A. Milner**, "How to fully integrate switching DC-DC supplies with inductor multipliers," *Planet Analog*, Dec. 18, **2005**.
- T21. G.A. Rincón-Mora and **N. Keskar**, "Cloaking the non-idealities of DC-DC converter stability," *Power Management Design Line (PMDL)*, Jan. 20, **2006**.
- T22. G.A. Rincón-Mora and **N. Keskar**, "Cloaking the non-idealities of DC-DC converter stability," *Planet Analog*, Jan. 20, **2006**.
- T23. **E. Torres** and G.A. Rincón-Mora, "Harvesting energy into lithium-ion batteries," *Power Management Design Line (PMDL)*, Feb. 14, **2006**.
- T24. **E.O. Torres** and G.A. Rincón-Mora, "Harvesting energy into lithium-ion batteries," *Planet Analog*, Feb. 14, **2006**.
- T25. **V. Gupta** and G.A. Rincón-Mora, "Reduce transistor mismatch errors without costly trimming and noisy chopping schemes," *Power Management Design Line (PMDL)*, Mar. 24, **2006**.
- T26. **V. Gupta** and G.A. Rincón-Mora, "Reduce transistor mismatch errors without costly trimming and noisy chopping schemes," *Planet Analog*, Mar. 24, **2006**.

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- T27. **P. Forghani** and G.A. Rincón-Mora, "Improve top-level simulation strategy for switching DC-DC converters," *Power Management Design Line (PMDL)*, Apr. 16, **2006**.
- T28. **P. Forghani** and G.A. Rincón-Mora, "Improve top-level simulation strategy for switching DC-DC converters," *Planet Analog*, Apr. 16, **2006**.
- T29. G.A. Rincón-Mora and **M. Chen**, "Li-Ion battery chargers – not just another design," *Power Management Design Line (PMDL)*, May 17, **2006**.
- T30. G.A. Rincón-Mora and **L.A. Milner**, "Can SoC switching regulators answer the challenge of their SiP counterparts?" *Power Management Design Line (PMDL)*, Jun. 21, **2006**.
- T31. G.A. Rincón-Mora and **J. Vogt**, "Fooling Faraday: On-chip capacitor multipliers," *Power Management Design Line (PMDL)*, Jul. 27, **2006**.
- T32. G.A. Rincón-Mora and **N. Keskar**, "Unscrambling the power losses in switching boost converters," *Power Management Design Line (PMDL)*, Aug. 18, **2006**.
- T33. G.A. Rincón-Mora and **E. Torres**, "Energy harvesting: A battle against power losses," *Power Management Design Line (PMDL)*, Sept. 23, **2006**.
- T34. G.A. Rincón-Mora and **E. Torres**, "Energy harvesting: A battle against power losses," *Planet Analog*, Oct. 8, **2006**.
- T35. G.A. Rincón-Mora and **V. Gupta**, "Bandgaps in the crosshairs: What's the trim target?" *Power Management Design Line (PMDL)*, Oct. 18, **2006**.
- T36. G.A. Rincón-Mora and **V. Gupta**, "Bandgaps in the crosshairs: What's the trim target?" *Planet Analog*, Oct. 18, **2006**.
- T37. G.A. Rincón-Mora and **M. Chen**, "Squeezing operational life out of a shrinking energy capsule," *Power Management Design Line (PMDL)*, Nov. 20, **2006**.
- T38. G.A. Rincón-Mora and **L. Milner**, "Inductors and multipliers in practice--Get efficient transfer of energy," *Power Management Design Line (PMDL)*, Jan. 1, **2007**.
- T39. G.A. Rincón-Mora and **J. Vogt**, "Self-powered wireless sensor nodes: Among other things, a load management feat," *Power Management Design Line (PMDL)*, Jan. 24, **2007**.
- T40. G.A. Rincón-Mora and **J. Vogt**, "Self-powered wireless sensor nodes: Among other things, a load management feat," *Planet Analog*, Jan. 24, **2007**.
- T41. G.A. Rincón-Mora and **J. Vogt**, "Self-powered wireless sensor nodes," *Electronic Components* (in Russian), no. 11, pp. 51-56, Nov. 15, **2007**.
- T42. **E.O. Torres**, **L.A. Milner**, and G.A. Rincón-Mora, "Hybrid supplies for wireless micro-systems," *The Electrochemical Society's Interface*, vol. 17, no. 3, pp. 57-60, Fall **2008**.

Texas Instruments Publications:

- TI1. G.A. Rincón-Mora, "Voltage References – Part I," 61 pg., Jul. **1998**.
- TI2. G.A. Rincón-Mora, "Voltage References – Part II," 71 pg., Jan. **1999**.
- TI3. G.A. Rincón-Mora, "Linear Regulators," 75 pg., Nov. **1999**.

Commercial Product Releases:

1. G.A. Rincón-Mora (Project Leader), **TPS2810** - CMOS driver, Released-to-production (RTP) **1995**.
2. G.A. Rincón-Mora (Project Leader), **TPS2811** - CMOS driver, RTP **1995**.
3. G.A. Rincón-Mora (Project Leader), **TPS2816** - CMOS driver, RTP **1995**.
4. G.A. Rincón-Mora (Project Leader), **TPS2817** - CMOS driver, RTP **1995**.
5. G.A. Rincón-Mora (Project Leader), **TPS2818** - CMOS driver, RTP **1995**.
6. G.A. Rincón-Mora (Project Leader), **TPS2819** - CMOS driver, RTP **1995**.
7. G.A. Rincón-Mora (Circuit Designer), **Viper** - BiCMOS Wireless Power Manag. IC, RTP **1995**.
8. G.A. Rincón-Mora (Circuit Designer), **Viper Lite** - BiCMOS Low Dropout Regulator, RTP **1996**.
9. G.A. Rincón-Mora (Circuit Designer), **Maverick** - BiCMOS Wireless Power Manag., RTP **1997**.
10. G.A. Rincón-Mora (Circuit Designer), **TPS912x** - BiCMOS Wireless Power Manag., RTP **1998**.
11. G.A. Rincón-Mora (Design Team Leader), **TPS5210** - Programmable BiCMOS PWM Controller, RTP **1998** - featured on *EDN's Top 100 Products* and on the cover of *Electronic Design*.
12. G.A. Rincón-Mora (Design Team Leader), **TPS5615** - BiCMOS PWM Controller, RTP **1998**.
13. G.A. Rincón-Mora (Design Team Leader), **TPS5618** - BiCMOS PWM Controller, RTP **1998**.

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14. G.A. Rincón-Mora (Design Team Leader), **TPS5625** - BiCMOS PWM Controller, RTP **1998**.
15. G.A. Rincón-Mora (Design Team Leader), **TPS5633** - BiCMOS PWM Controller, RTP **1998**.
16. G.A. Rincón-Mora (Design Team Leader), **SN104685DW** - BiCMOS PWM Controller, RTP **1998**.
17. G.A. Rincón-Mora (Design Team Leader), **TPS7415D** - CMOS Liner Reg., RTP **1999**.
18. G.A. Rincón-Mora (Design Team Leader), **TPS7418D** - CMOS Liner Reg., RTP **1999**.
19. G.A. Rincón-Mora (Design Team Leader), **TPS7425D** - CMOS Liner Reg., RTP **1999**.
20. G.A. Rincón-Mora (Design Team Leader), **TPS7430D** - CMOS Liner Reg., RTP **1999**.
21. G.A. Rincón-Mora (Design Team Leader), **TPS7433D** - CMOS Liner Reg., RTP **1999**.
22. G.A. Rincón-Mora (Design Team Leader), **TPS56100** - 5V BiCMOS PWM Controller, RTP **1999**.
23. G.A. Rincón-Mora (Circuit Designer/Tech. Advisor), **TPS56300** - BiCMOS Chrg Pump, RTP **1999**.
24. G.A. Rincón-Mora (Design Team Leader), **TPS5211** - BiCMOS 1MHz Hyst. Controller, RTP **1999**.
25. G.A. Rincón-Mora (Design Team Leader), **TPS5300** - BiCMOS Laptop PWM Controller, RTP **2001**.
26. G.A. Rincón-Mora (Circuit Designer), **MSP430's LDO** - BiCMOS Low Dropout Regulator, RTP **2004**.

Invited Short Courses:

1. (Only instructor: **1-day** short course, 17 participants), *Integrated DC-DC Converters: A Topological Journey! RF Micro-Devices in Greensboro*, North Carolina, Apr. **2002**.
2. (Only instructor: **3-day** short course, 18 participants), *Low Voltage, State-of-the-Art Integrated Power Management Circuits – A Top-Down Design Approach*. **Hong Kong Science and Technology Park**, Hong Kong, China, Aug. **2003**.
3. (Only instructor: **5-day** short course, 15 participants), *CMOS Analog Integrated Circuits*. **Georgia Tech** Global Learning and Conference Center, Atlanta, Georgia, Sept. 26-30, **2005**.
4. (Only instructor: **1-hour** short course), *Dynamically Adaptive Power Supply Circuits for Radio-Frequency (RF) Power Amplifier (PA) Applications*. Invited **IEEE Expert Now** (On-Line) Module, Fall **2005**.
5. (Only instructor: **4-day** short course, 23 participants), *Power Management Integrated Circuits – A Top-Down Design Approach*. **ON Semiconductor** in **Bratislava**, Slovakia, Dec. 12-15, **2005**.
6. (Only instructor: **4-day** short course, 22 participants), *Power Management Integrated Circuits – A Top-Down Design Approach*. **ON Semiconductor** in **Toulouse**, France, Dec. 19-22, **2005**.
7. (Only instructor: **4-day** short course, 20 participants), *Power Management Integrated Circuits – A Top-Down Design Approach*. **Navy: Space and Naval Warfare Systems Command**, **San Diego**, California, Apr. 10-13, **2006**.
8. (Only instructor: **4-day** short course, 20 participants), *Power Management Integrated Circuits – A Top-Down Design Approach*. **Cypress Semiconductor** in **Colorado Springs**, Colorado, Apr. 17-20, **2006**.
9. (Only instructor: **1-day** short course, 70 participants), *Linear Regulators - From the Ground Up...* **IEEE SSCS Distinguished Lecture** in **Hsinchu**, Taiwan, Jun. 8, **2006**.
10. (Only instructor: **1-day** short course, 40 participants), *Linear Regulators - From the Ground Up...* **IEEE SSCS Distinguished Lecture** in **Taipei**, Taiwan, Jun. 9, **2006**.
11. (Only instructor: **4-day** short course, 15 participants), *Power Management Integrated Circuits – A Top-Down Design Approach*. **Toko Inc.** in **Saitama**, Japan, Dec. 11, **2006**.
12. (Only instructor: **3-day** short course, 20 participants), *Power Management Integrated Circuits – A Top-Down Design Approach*. **Intel Corp.** in **Hillsboro**, Oregon, May 9-11, **2007**.
13. (Only instructor: **4-day** short course, 15 participants), *Power Management Integrated Circuits – A Top-Down Design Approach*. **Spyro Technology** in **Singapore**, May 21-24, **2007**.
14. (Only instructor: **4-day** short course, 41 participants), *Analog IC Design – An Intuitive Approach*. **Integrated Device Technology (IDT)** in **Atlanta**, Georgia, Jan. 26, Feb. 23, & Mar. 9 & 23, **2009**.

Invited Conference Seminar/Tutorial Presentations:

1. "Self-Oscillating Hysteretic V-Mode DC-DC Controllers: From the Ground Up," *IEEE's Power Electronics Specialists Conference (PESC)*, Vancouver, Canada, Jun. **2001**.
2. "Integrated LDOs: From the Ground Up!" *IEEE's International Symposium on Circuits and Systems (ISCAS)*, Scottsdale, Arizona, May **2002**.
3. "Integrated DC-DC Converters: A Topological Journey!" *IEEE's Midwest Symposium on Circuits and Systems (MWSCAS)*, Tulsa, Oklahoma, U.S.A., Aug. **2002**.

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4. "Dynamically Adaptive Power Supply Circuits for PA Wireless Applications," *IEEE's International Microwave Symposium (IMS)*, Long Beach, California, Jun. 2005.
5. "Hybrid Fuel Cell / Lithium-Ion Powered, Power Conscious SiP ICs," *1st International Workshop on 3S - SOP, SiP, SOC Electronic Technologies*, Atlanta, Georgia, Sept. 2005.
6. "AC Design and Performance of Low-Dropout Regulators (LDOs)," *IEEE's European Conference on Circuit Theory and Design (ECCTD)*, Sevilla, España, Aug. 30, 2007.
7. "Powering Micro-Systems with Fuel-Cell Hybrids," *10th Annual International Conference on Small Fuel Cells*, Atlanta, Georgia, Apr. 30, 2008.
8. "Low-Dropout Regulator (LDO) ICs," *IEEE's NEWCAS-TAISA Conference*, Montreal, Canada, Jun. 22, 2008.
9. "Powering Microsystems," *CMOS Emerging Technologies Workshop*, Vancouver, Canada, Sept. 25-27, 2009.
10. "Switching DC-DC Supplies and their Single-Inductor, Multiple-Output (SIMO) Derivatives," *IEEE's International Symposium on Circuits and Systems (ISCAS)*, Paris, France, May 30, 2010.
11. "Energy-Harvesting Switching Converter ICs," *International Workshop on Power Supply On Chip*, Cork, Ireland, Oct. 15, 2010.
12. "Energizing and Powering Microsystems," *The Materials Research Society (MRS) Fall Meeting*, Boston, Massachusetts, Nov. 29 – Dec. 2, 2010.
13. "Power-Management Systems on Chip (SoC) for Mobile Applications," *IEEE's International Conference on Microelectronics (ICM)*, Cairo, Egypt, Dec. 19 – 22, 2010.
14. "Power-Supply Circuits and Systems for Battery-Powered Devices," *IEEE's Very Large-Scale Integration Design, Automation and Test (VLSI-DAT)*, Hsinchu, Taiwan, Apr. 25 – 27, 2011.
15. "Energy-harvesting ICs," *IEEE European Solid-State Circuits Conference (ESSCIRC)*, Helsinki, Finland, Sept. 12 – 16, 2011.
16. "Energizing and Powering Microsystems," *IEEE Faible Tension Faible Consommation*, Paris, France, Jun. 6 – 8, 2012.
17. "Harvesting ICs," *CMOS Emerging Technologies Workshop*, Vancouver, Canada, Jul. 18 – 20, 2012.
18. "Energizing and Powering Microsystems," *SHPE National Conference*, Ft. Worth, Texas, Nov. 14 – 18, 2012.

Invited Seminar Presentations:

1. "Integrated Power Management Circuits." **National Semiconductor**, Santa Clara, Dec. 2002.
2. "Power Conscious ICs." **Texas A & M University**, College Station, Jun. 21, 2004.
3. "El Mundo es Análogo, y las Oportunidades son Muchas." **University of Puerto Rico** at Mayagüez, Oct. 18, 2004.
4. "Hybrid Fuel Cell / Lithium-Ion Powered, Power Conscious ICs." **National Semiconductor**, Santa Clara, Jun. 2005.
5. "Microsystems: Power and Energy." Army Research Lab (ARL) Advanced Microsystems Workshop, Virginia, Jan. 30, 2006.
6. "Self-Sustaining, Self-Powered, Energy and Power Conscious ICs for Micro-Scale Devices," **Universitat Politècnica de Catalunya**, Barcelona, Spain, Jul. 10, 2006.
7. "Self-Powered, Self-Sustaining System-on-Chip (SoC) and System-in-Package (SiP) Power Solutions," *National Science Foundation (NSF) and Intelligence Community Workshop on Micro-Scale Power Sources*, Virginia, Apr. 24-25, 2007.
8. "Powering Micro-Systems," **National Semiconductor**, Santa Clara, California, Nov. 30, 2007.
9. "AC Design and Performance of Low-Dropout Regulators (LDOs)," **Texas A & M University** at College Station, Jun. 9, 2008.
10. "Power Losses in Switching DC-DC Converter ICs," **Texas A & M University** at College Station, Jun. 9, 2008.
11. "Powering Micro-Systems," **Shanghai Jiao Tong University**, China, Oct. 8, 2008.
12. "Powering Micro-Systems," **Linear Technology**, San Jose, California, Feb. 13, 2009.
13. "Powering Micro-Systems," *IEEE CASS Distinguished Lecture*, Montreal, Canada, Jul. 17, 2009.
14. "Energizing and Powering Microsystems," *IEEE Electron Device Society Chapter*, Vancouver, Canada, Sept. 24, 2009.
15. "Harvesting Ambient Energy in Miniaturized Systems," *SRC TxACE's Energy and Power Analog Circuit Challenges Workshop*, Dallas, Texas, Sept. 28, 2009.
16. "Single-Inductor Multiple-Output Switching DC-DC Converters," **Inha University**, Incheon, Korea, Nov. 19, 2009.
17. "Single-Inductor Multiple-Output Switching DC-DC Converters," **Samsung**, Seoul, Korea, Nov. 20, 2009.
18. "Power Management ICs for Portable Devices," **University of Seoul**, Korea, Feb. 17, 2010.

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19. "Energizing and Powering Microsystems," Electronics and Telecommunications Research Institute (**ETRI**), Daejeon, Korea, Feb. 18, **2010**.
20. "Power Management ICs for Portable Devices," **Silicon Works Co.-Daejeon**, Korea, Feb. 18, **2010**.
21. "Energizing and Powering Microsystems," Korea Advanced Institute of Science and Technology (**KAIST**), Daejeon, Korea, Feb. 19, **2010**.
22. "Harvesting Ambient Energy in Miniaturized Systems," *IT Convergence Research Project Workshop* at **KAIST**, Daejeon, Korea, Feb. 19, **2010**.
23. "Harvesting Kinetic Energy in Miniaturized Systems," *IEEE's CASS Distinguished Lecture*, Montreal, Canada, Sept. 17, **2010**.
24. "Energizing and Powering Microsystems," **Texas Instruments**, Dallas, Texas, Oct. 25, **2010**.
25. "AC Design and Performance of Low-Dropout Regulators (LDOs)," *IEEE's CASS Distinguished Lecture*, Mayagüez, Puerto Rico, Nov. 12, **2010**.
26. "Harvesting Kinetic Energy in Miniaturized Systems," **National Taiwan University**, Taipei, Taiwan, Nov. 23, **2010**.
27. "Energizing and Powering Microsystems," **IEEE SSCS Hsinchu Chapter**, Taiwan, Nov. 24, **2010**.
28. "AC Design and Performance of Low Dropout Regulators," **IEEE CASS Distinguished Lecture**, Tainan, Taiwan, Nov. 26, **2010**.
29. "Power Losses in Switching DC-DC Converter ICs," National Cheng Kung University (**NCKU**), Tainan, Taiwan, Nov. 26, **2010**.
30. "Energy-harnessing ICs," National Semiconductor Corporation (**NSC**), Santa Clara, California, July 15, **2011**.
31. "AC Design and Performance of LDOs," **IEEE CASS Taipei Chapter**, Hsinchu, Taiwan, Sept. 5, **2011**.
32. "Energy-harnessing ICs," ON Semiconductor (**ON**), Phoenix, Arizona, Oct. 4, **2011**.
33. "Energy-harnessing ICs," Texas Instruments (**TI**), Dallas, Texas, Oct. 5, **2011**.
34. "Energy-harnessing ICs," IEEE Industrial Electronics, Power Electronics, and Industry Applications Society Taipei Chapters in National Tsing Hua University, Hsinchu, Taiwan, Dec. 9, **2011**.
35. "Frequency Response of Switching DC–DC Converters," National Cheng-Kung University, Tainan, Taiwan, Dec. 14, **2011**.
36. "Feedback Control of Switching DC–DC Converters," National Cheng-Kung University, Tainan, Taiwan, Dec. 14, **2011**.
37. "Energy-harvesting Microchips," IEEE Power Electronics Society (PELS) and IEEE Life Members' Chapters, Atlanta, Georgia, Mar. 28, **2012**.

Invited Keynote Addresses and Speeches:

1. "Orgullo Hispano," **Robins Air Force Base**, Sept. 23, **2003**.
2. "Robins AFB Hispanic Heritage Luncheon," **Robins Air Force Base**, Oct. 3, **2005**.
3. "Energy and Power Management Trends," **Analog Leaders Forum**, Seoul, Korea, Oct. 16, **2009**.
4. "Energizing and Powering Microsystems," *IEEE's International SoC Design Conference (ISOCC)*, Busan, Korea, Nov. 23, **2009**.
5. "Energy-Harvesting ICs – The State of the Art," *IEEE's Circuits and Systems for Medical and Environmental Applications (CASME)*, Merida, Mexico, December 13 – 15, **2010**.

Expert Conference Panelist (Invited):

1. Expert Panelist for "Power Management for SoCs," *IEEE VLSI Symposium*, Hawaii, Jun. 15-17, **2006**.

Non-Engineering Publications:

1. G.A. Rincón-Mora, "Strawberry Delight" [poem], *Forgotten Moments* (ISBN: 1-58235-159-7), Editor's Choice Award, 2000.
2. G.A. Rincón-Mora, "Ojitos Verdes" [poem], *Nuevo Impacto*, Oct. 2002.
3. G.A. Rincón-Mora, "A Christmas Tale" [short story], *ISB Cafe* (www.InternationalStoryBook.com), Dec. 2002.
4. G.A. Rincón-Mora, "Mi Querida Daniela" [short story], *Shades Of Romance Magazine (SORM)* (<http://www.sormag.com>), Jan. – Feb. 2003.
5. G.A. Rincón-Mora, "Mi Querida Daniela" [short story], *ECESIS* (<http://www.ece.gatech.edu/ecesis/>), Spring 2004.
6. G.A. Rincón-Mora, "Flor Andina" [poem], *Nuevo Impacto*, Jul. 2004.

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7. G.A. Rincón-Mora, "The Bund" [photograph] - *Chapter & Verse - A Publication of the Hong Kong International Literary Festival Ltd.*, 2004.
8. G.A. Rincón-Mora, "Little Lots" [poem], ECESIS (<http://www.ece.gatech.edu/ecesis/>), Spring 2005.
9. G.A. Rincón-Mora, "Just passing through (Island of Idra)" [photograph] – *Photographers of Greece* (http://grecja.home.pl/eng/efotograficy_grecji.htm).
10. G.A. Rincón-Mora, "Working and Teaching in Tanzania," *Volunteers for Peace (VFP) - Tanzania* (<http://www.vfpcanada.org/Tanzania.pdf>), Nov. 2005.
11. G.A. Rincón-Mora, "Let me...let me in there! (Mwanga, Tanzania)," *Volunteers for Peace - VFP 2006 Newsletter* (<http://www.vfp.org/2006NL.htm>), Jan. 2006.
12. G.A. Rincón-Mora, "Home!" [photograph] - ECESIS (<http://www.ece.gatech.edu/ecesis/>), Spring 2006.
13. G.A. Rincón-Mora, "Mind and Heart" [poem] - ECESIS (<http://www.ece.gatech.edu/ecesis/>), Spring 2007.
14. M. Cheng, G.A. Rincón-Mora, and G. Heaney, "Volunteers For Peace - Vermont non-profit serving the world," *Wishtank, Journal of Intellectual Freedom*, Jun. 2007.
15. G.A. Rincón-Mora, "The Girl in White" [poem] - ECESIS (<http://www.ece.gatech.edu/ecesis/>), Spring 2008.

IV. Teaching

Ph.D. Students Graduated:

1. **Biranchi Sahu**, *Dynamically Adaptive Supplies for Linear RF Power Amplifiers*, Ph.D. Dec. **2004**.
2. **Pooya Forghani**, *Lossless Current-Sensor IC for Switching DC-DC Converters*, Ph.D. Jun. 1, **2006**.
3. **Vishal Gupta**, *An Accurate, Trimless, High PSRR, Low-Voltage, CMOS Reference IC*, Ph.D. Jul. 3, **2007**.
4. **Neeraj Kesar**, *High-Bandwidth, Wide LC-RESR Compliant $\Sigma\Delta$ Boost DC-DC Converters*, Ph.D., Mar. 24, **2008**.
5. **Erick O. Torres**, *An Electrostatic CMOS/BiCMOS Vibration-Based Harvester-Charger IC*, Ph.D., May 4, **2010**.

Master Students Graduated:

1. **Mark Guildersleeve**, *Low Voltage Power Saving Techniques for DC-DC Converters*, M.S.E.E., Aug. **2002**.
2. **Abbas Poonawala**, *Precision, Low-Voltage, Integrated Capacitor Multipliers*, M.S.E.E., Dec. **2003**.
3. **Aditya Makharia**, *Inductorless DC-DC Converters for Portable Applications*, M.S.E.E., Dec. **2003**.
4. **Oscar Palomino**, M.S.E.E., Dec. **2007**.
5. **Amisha Manek**, M.S.E.E., Dec. **2008**.
6. **Justin Vogt**, *nW Analog-Digital Converter for Blood-Glucose Monitors*, M.S.E.E., Dec. **2008**.
7. **Amit Patel**, Thesis: "High PSR Low Dropout Regulator ICs," M.S.E.E., May **2009**.
8. **Priyanka Lakhe**, M.S., May **2010**.
9. **Tim Guglielmo**, M.S., May **2011**.

Ph.D. Students Currently Advised:

1. **Luke Milner**, *SiP Inductor-Based Switching Supplies*.
Start: Spring '04, Preliminary Exam: Spring '05, Proposal: Fall '09, Expected Grad.: Spring '12.
2. **Suhwan Kim**, *Hybrid Fuel Cell-Lithium Ion Charger-Supply-Mixer IC*.
Start: Fall '06, Preliminary Exam: Fall '05, Proposal: Spring '11, Expected Grad.: Spring '12.
3. **Dongwon Kwon**, *Micro-Scale Piezoelectric Driver/Harvester IC*.
Start: Spring '07, Preliminary Exam: Fall '06, Proposal: Summer '11, Expected Grad.: Spring '12.
4. **Orlando Lazaro**, *Wireless Inductively-Coupled Charger-Supply CMOS IC*.
Start: Fall '07, Preliminary Exam: Fall '08.
5. **Rajiv Damodaran**, *CMOS Light-Harvesting ICs for Miniaturized Photovoltaic Cells*.
Start: Fall '09, Preliminary Exam: Spring '09.
6. **Carlos Solís**, *Single-Inductor, Multiple-Output Switching Micro-Watt CMOS Supplies*.
Start: Fall '10, Preliminary Exam: Fall '10.
7. **Andres Blanco**, *Thermal Energy-Harvesting ICs*.
Start: Spring '11, Preliminary Exam: Fall '10.

Undergraduate Students Advised:

1. **R. Dokania** (Intern from India: Summer '02), *Cancellation of Load Regulation in Low Drop-out Regulators*.

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2. **K. Dash** (Intern from India: Summer '03), *Active Bulk Capacitor Multipliers*.
3. **Carlos Cubero Ponce** (Intern from University of Puerto Rico: Summer '05), *Drain Follower Buffer*.
4. **Freddie Alequín Ramos** (Intern from University of Puerto Rico: Summer '07), *System-in-Package Integration*.
5. **LaVonda Brown** (Intern from Norfolk: Summer '08), *Piezoelectric Modeling*.
6. **Adilson Cardoso** (Georgia Tech: Fall '06 – Fall '07).

Visiting Scholars Advised:

1. **H.I. Pan** (Ph.D. student from University of Taiwan in Taipei, Taiwan: Jan. to Dec. 2005), *Asynchronous Power-Tracking Supplies for RF PAs*.

Course Developed (at Georgia Tech):

ECE 8813 – Power IC Design (first developed and offered in Fall '09): Model, analyze, and design power-conditioning ICs.

Professional Four-day Short Courses Developed:

Analog IC Design – An Intuitive Approach (first developed and offered at Georgia Tech Global Learning and Conference Center in Sept. 26-30, 2005): Model, analyze, and design analog ICs.

Power IC Design – From the Ground Up (first developed and offered in Bratislava, Slovakia, for ON Semiconductor in Dec. 12-15, 2005): Survey, model, analyze, and design power-management and -conditioning ICs.

Courses Taught (for Georgia Tech):

1. **ECE 3040 – Microelectronic Circuits**: Spring '02, '03.
2. **ECE 3050 – Analog Electronics**: Fall '01, '02, '03, '04, '05, '06, '07, '10, Spring '05, '06.
3. **ECE 4430 – Analog Integrated Circuits**: Fall '02, '03.
4. **ECE 6412 – Analog Integrated Circuit Design**: Spring '04, '07, '08, '09, '10, '11, and at Shanghai Jiao Tong University, Fall '08, Fall '11.
5. **ECE 8813 – Power Integrated Circuit Design**: Fall '09 (at Shanghai Jiao Tong University).

V. Service

Professional Leadership:

1. **Chapter Vice-Chair**, Atlanta's IEEE Solid-State Circuits and Circuits and Systems Society (**SSCS–CASS**), **2003 – 2004**.
2. **Chapter Chair**, Atlanta IEEE Solid-State Circuits and Circuits and Systems Society (**SSCS–CASS**), **since 2004**.
3. **Technical Program Co-Chair**, IEEE Midwest Symposium on Circuits and Systems (**MWSCAS**), Puerto Rico, **2006**.
4. **Technical Program Chair**, Joint IEEE 50th Midwest Symposium on Circuits and Systems (**MWSCAS**) and 5th IEEE **NEWCAS**, Montreal, **2007**.
5. **Circuit Design Vice Chair**, IEEE International Caribbean Conference on Devices, Circuits and Systems (**ICCDCS**), Cancun, Mexico, **2008**.
6. **Guest Co-Editor**, *Analog Integrated Circuits and Signal Processing Journal (AICSP)*, Special Issue on Analog and RF, Aug. **2009**.
7. **Associate Editor**, *IEEE Transactions on Circuits and Systems II (TCAS II)*, **2007 – 2009** and **2010 – 2011**.
8. **Associate Editor**, *IEEE Journal of Solid-State Circuits (JSSC)*, **2011**.
9. **Editorial Board Member**, *Journal of Low-Power Electronics (JOLPE)*, **2009 – 2011**.
10. **General Chair**, Energy and Power Integrated Circuits Workshop, SRC Texas Analog Center of Excellence (**TxACE**), Sept. 28-29, **2009**.
11. **Special Session Co-Organizer**, "Emerging Energy and Power Integrated Circuits," *IEEE International Symposium on Circuits and Systems (ISCAS)*, Rio de Janeiro, Brazil, May **2011**.
12. **Guest Editor**, *IEEE Transactions on Circuits and Systems II (TCAS II)*, Special Issue on Energy Harvesting, Dec. **2011**.
13. **Technical Program Co-Chair**, IEEE International SoC Conference (**ISOCC**), Jeju, Korea, **2011**.

Committee Membership:

1. **Technical Program Committee**, IEEE Southwest Symposium on Mixed-Signal Design (**SSMSD**), **2002**.
2. **Selection Committee Review Panel**, NSF SBIR/STTR Committee on Power Management, Mar. **2003**.
3. **Selection Committee Review Panel**, NSF SBIR/STTR Committee on Power Management, Sept. **2003**.
4. **Technical Committee**, IEEE's CASS Analog Signal Processing (**ASP**), **since 2003**.
5. **Selection Committee Review Panel**, NSF SBIR/STTR Committee on Signal Processing & IC Design, Oct. **2004**.

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6. **Selection Committee Review Panel**, NSF SBIR/STTR Committee on IC Design: Testing, Aug. **2005**.
7. **Steering Committee**, IEEE Midwest Symposium on Circuits and Systems (**MWSCAS**), since **2006**.
8. **Selection Committee Review Panel**, NSF SBIR/STTR Committee on IC Design I, Feb. **2007**.
9. **Technical Committee**, IEEE's CASS Power and Energy Circuits and Systems (**PECAS**), since **2009**.
10. **Fellows Evaluation Committee**, IEEE's Circuits and Systems Society (**CASS**), **2011**.
11. **Distinguished Lecturer Committee**, IEEE's Circuits and Systems Society (**CASS**), **2011 – 2012**.

Professional Membership:

1. **IEEE (Fellow '11**, Senior Member '01, Member '97, Student Member '90), **IET/IEE (Fellow '09**, Member '06), and **SHPE** Society of Hispanic Professional Engineers (**Life Member '00**).

International Ph.D. Committee:

1. [Rapporteur, Jury] Vincent Telandro, *On-Chip Voltage Regulator Protecting Against Power Analysis Attacks*, Laboratoire Matériaux et Microélectronique de Provence, Institut Supérieur d'Electronique du Nord, France, Nov. 2007.

At Georgia Tech:

1. **ECE Graduate Student Recruitment Committee**, Member, **2001 – 2003** and **2004 – 2005**.
2. **ECE Student-Faculty Committee**, Member: **2003 – 2004**, **2005 – 2006**, **2006 – 2007**, **2007 – 2008**, and **2011 – 2012**;
Chair: 2008 – 2009, **2009 – 2010**, and **2010 – 2011**.
3. **ECE Student Award Committee**, Member: **2006**.
4. **ECE Georgia Power Distinguished Professor Search Committee**, Member, **2006**.
5. **Freshmen Partner** for Freshmen Partnership Program, **2006**.
6. **Outstanding Electrical and Computer Engineering Senior Student Awards Committee**, **2009**, **2010**, and **2011**.
7. **Student Awards Committee**, **2010**.
8. **Ph.D. Committees:**

	Student	Proposal Committee	Reading Committee	Defense Committee	Ph.D.
1	Sidharth Dalmia	Chair: 3/14/02			Ph.D.
2	Zhiwei Dong	N/A	Member: 7/15/02	Member: 7/15/02	Ph.D.
3	Theocharis Boukas	Chair: 8/12/02	N/A	Member: 03/26/03	Ph.D.
4	Susanta Sengupta	Member: 4/15/02	Member: 07/08/04	Member: 07/08/04	Ph.D.
5	Kyu-won Choi	Chair: 10/29/02	Member: 09/09/03	Member: 09/09/03	Ph.D.
6	Woopoung Kim	Chair: 4/30/03			Ph.D.
7	Biranchinath Sahu	Advisor: 3/24/04	Chair: Fall 11/4/04	Chair: Fall 11/4/04	Ph.D.
8	Bhyrav Mutnury	Member: 1/28/05			Ph.D.
9	Pooya Forghani	Advisor: 6/24/04	Chair: 6/1/06	Chair: Sum. 6/1/06	Ph.D.
10	Vishal Gupta	Advisor: 9/20/05	Chair: 7/3/07	Chair: 7/3/07	Ph.D.
11	Neeraj Keskar	Advisor: 9/20/05	Chair: 3/24/08	Chair: 3/24/08	Ph.D.
12	Jau-Horng Chen	Chair: 9/22/05	Member: 5/25/06	Member: 06/30/06	Ph.D.
13	Soumendu Bhattacharya			Member: 06/23/05	Ph.D.
14	Jacob Minz	Member:		Member:	Ph.D.

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		10/19/05		07/19/06	
15	Shruti Prakash	Member: 7/27/06	Member: 03/04/09	Member: 03/04/09	Ph.D.
16	Kenta Nakayashiki	Member: 9/28/06	Member: 10/2/07	Member: 10/2/07	Ph.D.
17	Ripal Nathuji	Member: 5/2/07			Ph.D.
18	Rajeswari Chandrasekaran	Member: 08/22/07		Member: 7/15/10	Ph.D.
19	David Pritchett	Member: 12/13/07		Member: 2/4/09	Ph.D.
20	N. Lalgudi Subramanian	Member: 1/17/07		Member: 3/26/08	Ph.D.
21	Erick Torres	Advisor: 4/9/08	Chair: 5/4/10	Chair: 5/4/10	Ph.D.
22	Krishna Bharath	Member: 4/21/08			Ph.D.
23	Muhammad Nisar	Member: 7/30/08			Ph.D.
24	Dale Scott Douglas		Member: Fall 08		M.S.
25	Tahir Zaidi	Member: 6/1/09			Ph.D.
26	Luke Milner	Advisor: 7/15/09			Ph.D.
27	Suhwan Kim	Advisor: 3/13/11			Ph.D.
28	Sang Taek Han	Member: 3/3/11			Ph.D.
29	Mauricio Pardo Gonzalez	Chair: 4/29/11	Member: 1/18/12	Member: 1/18/12	Ph.D.
30	Debrup Das	Member: 6/1/11			Ph.D.
31	Dongwon Kwon	Advisor: 7/21/11			Ph.D.
32	Hengzhao Yang	Member: 2/23/12			Ph.D.

Community Service:

1. **Volunteers for Peace (VFP)**, Kigonigoni, **Tanzania** (school and levy construction), Summer (2 weeks) **2006**.
2. **Volunteers for Peace (VFP)**, Bangalore, **India** (teach children with AIDS and disabilities), Summer (2w) **2008**.
3. **Service Civil International (SCI)**, Ulaan Baatar, **Mongolia** (construction and farming at orphanage), Summer (2w) **2009**.
4. **Service Civil International (SCI)** in Viet Tri, **Vietnam** (teach orphans English), Summer (2w) **2010**.

VI. Honors, Awards, and Visibility

Awards and Distinctions:

1. Dr. Rincón-Mora's TPS5210, "**Top 100 Products**" of **1998** by **EDN** (on cover of *Electronic Design*).
2. One of **Top 7 Most Cited TCAS II Papers in 1998**: B.J. Blalock, P.E. Allen, and G.A. Rincón-Mora, "Designing 1V Op Amps Using Standard Digital CMOS Technology," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 45, no. 7, pp. 769-780, Jul. 1998.
3. **1999 TIDN Forum's "Significant TI Contributor."**
4. **TI's "Three Year Patent Award"** for U.S. 5,491,437; U.S. 5,500,625; and U.S. 5,519,341, **1999**.

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5. **Adjunct Professor** for the Georgia Institute of Technology (**Georgia Tech**), **1999 – 2001**.
6. **Florida International University's "Charles E. Perry Visionary Award," 2000.**
7. Inducted into **Georgia Tech's "Council of Outstanding Young Engineering Alumni," 2000.**
8. Society of Hispanic Professional Engineers' (SHPE) "**Hispanic in Technology Award," 2000.**
9. Voted by *Hispanic Business* into its list of "**The 100 Most Influential Hispanics," 2000.**
10. One of IEEE's **Top 25 Most Downloaded TCAS II Papers in 2004**: G.A. Rincón-Mora and R. Stair, "A low voltage, rail-to-rail, class AB CMOS amplifier with highdrive and low output impedance characteristics," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 48, no. 8, pp. 753-761, Aug. 2001.
11. Elevated to **Senior Member** of the Institute of Electrical and Electronics Engineers (**IEEE**), **2001**.
12. "**State of California Commendation Certificate**" from Lieutenant Governor Cruz M. Bustamante, **2001**.
13. **Robins Air Force Base's "Orgullo Hispano Award"** in Sept. 23, **2003**.
14. One of IEEE's **Top 200 Most Downloaded Journal Papers in 2004** (177 times in one month): B. Sahu and G.A. Rincón-Mora, "A High-Efficiency Linear RF Power Amplifier With a Power-Tracking Dynamically Adaptive Buck-Boost Supply," *IEEE Transactions on Microwave Theory and Techniques (TMTT)*, vol. 52, no. 1, pp. 112-120, Jan. 2004.
15. Hispanic Engineer National Achievement Awards Corporation's "**HENAAC Role Model of the Week**" in Jul. 5, **2005**.
16. "**7th Most Read** Power Management Design Line How-To Article in 2005" for G.A. Rincón-Mora and V. Gupta, "Power Supply Ripple Rejection and Linear Regulators: What's all the noise about?" *Power Management Design Line (PMDL)*, Sept. 20, **2005**.
17. **Robins Air Force Base's "Hispanic Heritage Award"** in Oct. 3, **2005**.
18. "**2nd Most Read** Power Management Design Line How-To Article in 2006" for E. Torres and G.A. Rincón-Mora, "Harvesting energy into lithium-ion batteries," *Power Management Design Line (PMDL)*, Feb. 14, **2006**.
19. **IEEE's CASS MWSCAS-NEWCAS, "IEEE CASS Service Award," Aug. 8, 2007.**
20. **2nd Place Award** for **2009** Science Applications International Corporation's Georgia Tech Paper Competition for D. Kwon and G.A. Rincón-Mora, "A Rectifier-Free Piezoelectric Energy Harvester Circuit."
21. *Solid-State Circuits Magazine* (Spring 2010) reported Rincón-Mora's *Analog IC Design with Low-Dropout Regulators* as one of two **best sellers at IEEE International Solid-State Circuits Conference (ISSCC) 2009**.
22. Elevated to **Fellow** of the Institution of Engineering and Technology (**IET**) in **2009**.
23. Elected **Distinguished Lecturer** by **IEEE's CASS** for **2009 – 2010**.
24. **Georgia Tech's "Thank a Teacher"** certificate, **2010**.
25. Elevated to **Fellow** of the Institution of Electrical and Electronics Engineers (**IEEE**) in **2011** "for contributions to energy and power integrated circuit design."

Magazine Covers featuring Dr. Rincón-Mora:

1. "Bravo – National Award Winners," *Official Magazine of the Society of Hispanic Professional Engineers*, Spring **2000**.
2. "The 100 Most Influential Hispanics," *Hispanic Business* magazine, Oct. **2000**.
3. "A high-tech engineer with a low-tech lifestyle," *La Fuente* (Dallas publication), Mar. **2000**.
4. "Gabriel Rincón-Mora - Impacta en la alta tecnología," *Nuevo Impacto* (Atlanta publication), Aug. **2002**.
5. "Profesionales Latinos – La nueva cara de Georgia," *Nuevo Impacto* (Atlanta publication), Oct. **2003**.
6. "Gabriel Rincón Mora – Un ingeniero polifacético: Inventor, profesor, escritor y actor" - "Gabriel Rincón Mora – Outstanding Engineer and Writer," *Nuevo Impacto* (Atlanta publication), Nov. **2004**.

Feature Stories about Dr. Rincón-Mora:

1. "Passion for design, apathy for gizmos," *Electronic Engineering Times*, Jun. **2000**.
2. "Designer has passion for work, apathy for gizmos," *Planet Analog*, Jun. **2000**.
3. "By Day an Engineer," *Intown* (Atlanta publication), Aug. **2002**.
4. "Notar – Short Stories and Poems to Boot," *Official Magazine of the Society of Hispanic Professional Engineers*, Aug. **2002**.
5. "Innovators Matter," *Hispanic Business* magazine, Sept. **2002**.
6. "Innovators Matter," *Hispanic Business* magazine, Dec. **2002**.
7. "Hispanic Engineering Talent," *Georgia Tech Society of Professional Hispanic Engineers*, Feb. **2003**.
8. "World-class training workshop on analog IC power management by top Integrated Circuit (IC) expert from the United States," *Hong Kong Science and Technology Parks News & Newsletter*, Oct. **2003**.

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9. "SSCS Subsidizes Short Course on Linear Regulator Design in Taipei," *IEEE Solid-State Circuits Society Newsletter*, Sept. 2006.
10. "Alumni Profile: Gabriel A. Rincón-Mora," *Summa Cum Laude*, Florida International University Honors College, Winter 2011, vol. 1, no. 3.

Other Awards and Recognitions:

1. *Presidential Academic Fitness Award*, (signed by President George Bush, Sr.), 1989.
2. *Insignis Scholarship*, University of Detroit, 1989.
3. *Phi Kappa Phi* (national honor society), 1991.
4. *Dean's List*, Florida International University, 1989 – 1992.
5. *B.S.E.E. with High Honors*, Florida International University, 1992.
6. *Florida Undergraduate Scholars Fund Scholarship*, State of Florida, 1989 – 1992.
7. *Faculty Scholars Scholarship*, Florida International University, 1989 – 1992.
8. *Honorary Award Recognition*, National Dean's List, 1990 – 1992.
9. *Eta Kappa Nu* (national electrical engineering honor society), 1992.
10. *Honorable Mention*, National Science Foundation (NSF), 1993.
11. *Tau Beta Pi* (Life Member: national engineering honor society), 1994.
12. *Outstanding Ph.D. Graduate*, Georgia Tech, 1996.