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Rincón-Mora Named NAI Fellow

January 4, 2018 Atlanta, GA



Gabriel A. Rincon-Mora

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Gabriel Alfonso Rincón-Mora, a professor in the Georgia Tech School of Electrical and Computer Engineering (ECE), has been named a Fellow of the National Academy of Inventors (NAI). He is part of the Class of 2017 NAI Fellows, consisting of 155 renowned academic inventors who will be inducted during the Seventh Annual Conference of the National Academy of Inventors. The conference will take place on April 4-6, 2018 in Washington, D.C. <u>2012 News</u>

2011 News

2010 News

2009 News

<u>2008 News</u>

A member of the ECE faculty since 2001, Rincón-Mora is being recognized for his work in energy-harvesting and powerconditioning microchips. He was a design consultant at Texas Instruments from 2001-2003 and director of the Georgia Tech Analog Consortium from 2001-2004. Prior to his tenure on the ECE faculty, Rincón-Mora was adjunct professor at Georgia Tech from 1999-2001, senior design engineer and design team leader at Texas Instruments from 1997-2001, and circuit designer at the same company while he was a Ph.D. student at Georgia Tech from 1994-1996. He earned his M.S. and Ph.D. degrees in electrical engineering at Georgia Tech in 1994 and 1996.

Rincón-Mora currently leads the Georgia Tech Analog, Power, and Energy ICs Lab. He holds 25 U.S. patents and 17 foreign patents – all assigned/licensed. They have been incorporated into portable consumer products like cellular phones, laptops, and tablets since 1994. He has published nine books, four book chapters, and over 170 articles; designed over 26 commercial chips; and delivered over 125 international talks.

Rincón-Mora is a Fellow of the IEEE and a Fellow of the Institution of Engineering and Technology. He was recently named Distinguished Lecturer for the IEEE Circuits and Systems Society (CASS) for 2018-19, the second time that he has received this honor. He will speak on the topics of energizing and powering microsystems and energyharvesting power supplies. He also serves as technical program committee co-chair for the IEEE International Symposium on Circuits and Systems (ISCAS), to be held May 26-29, 2019 in Sapporo, Japan.

Rincón-Mora is the recipient of the National Hispanic in Technology Award from the Society of Hispanic Professional Engineers, the Charles E. Perry Visionary Award from Florida International University, a Commendation Certificate from the Lieutenant Governor of California, the IEEE Service Award from IEEE CASS, the Orgullo Hispano and the Hispanic Heritage awards from Robins Air Force Base, a Certificate of Appreciation from IEEE CASS, and two Thank a Teacher Certificates from Georgia Tech. Georgia Tech has also inducted him into its Council of Outstanding Young Engineering Alumni and *Hispanic Business* magazine named him one of "The 100 Most Influential Hispanics."

Election to NAI Fellow status is the highest professional accolade bestowed to academic inventors who have demonstrated a prolific spirit of innovation in creating or facilitating outstanding inventions that have made a tangible impact on quality of life, economic development, and the welfare of society. To learn more about the 2017 class of NAI Fellows, <u>visit the NAI website</u>.

Media Contact

Related Links

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