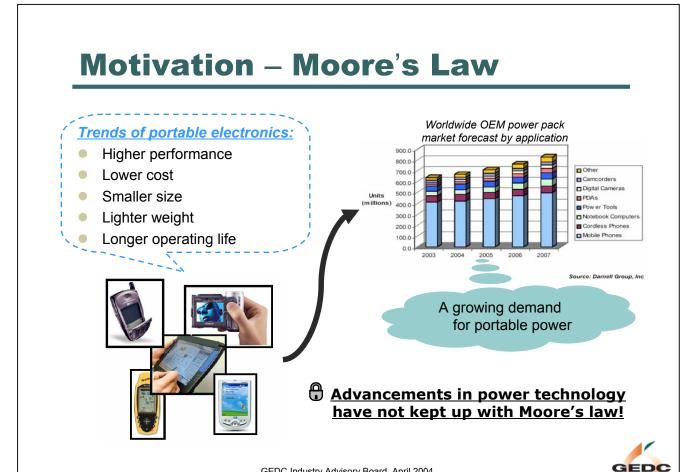
A Monolithic, Self-Powered IC with Fully Integrated Micro-Fuel Cell

Min Chen Advisor: Prof. G.A. Rincón-Mora Georgia Tech Analog and Power IC Design Laboratory School of Electrical and Computer Engineering Georgia Institute of Technology

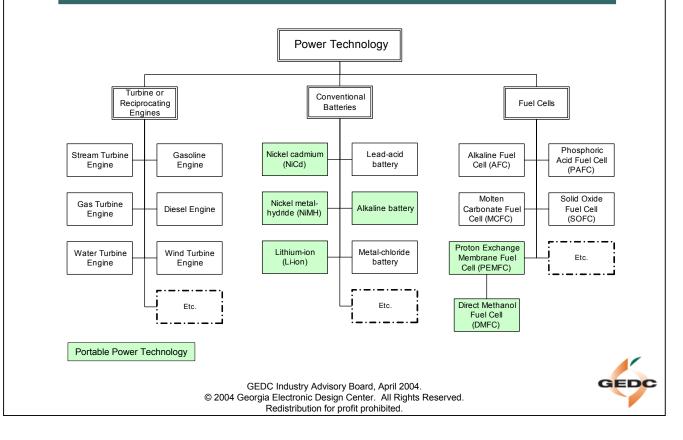
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Assortment of Power Technology



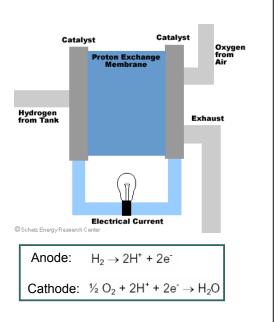
Introduction of Fuel Cells

- What is a Fuel Cell?
 - An electrochemical energy conversion device that converts <u>fuel</u> (e.g. hydrogen) and <u>oxidant</u> (e.g. oxygen) into water, producing <u>electricity</u>
- How Fuel Cells work?

 $\rightarrow \rightarrow \rightarrow \rightarrow$

- <u>Applications:</u> Portable power; Auxiliary power; Transportation power; Stationary power
- <u>Classifications</u>: Alkaline-, Phosphoric acid-, Molten carbonate-, Solid oxide-, Proton exchange membrane-fuel cell

* The PEM Fuel Cell Animation courtesy of Schatz Energy Research Center





Direct Methanol Fuel Cell (DMFC)

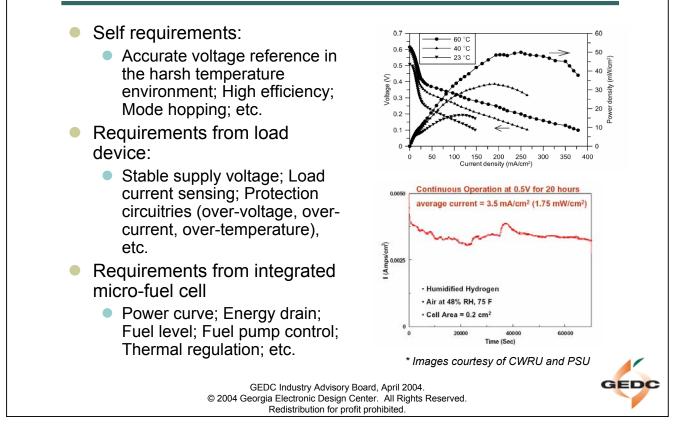
A variation of PEMFC, suitable for portable electronics Advantages: Use methanol instead of hydrogen to eliminate fuel reformer and store fuel easily Potentially high-energy density Environmentally-friendly High efficiency (~40%) Ambient temperature (50-130°C) Disadvantages: High overpotentials
→ slow response time Bulky size High cost GEDC Industry Advisory Board, April 2004. © 2004 Georgia Electronic Design Center. All Rights Reserved. Redistribution for profit prohibited.

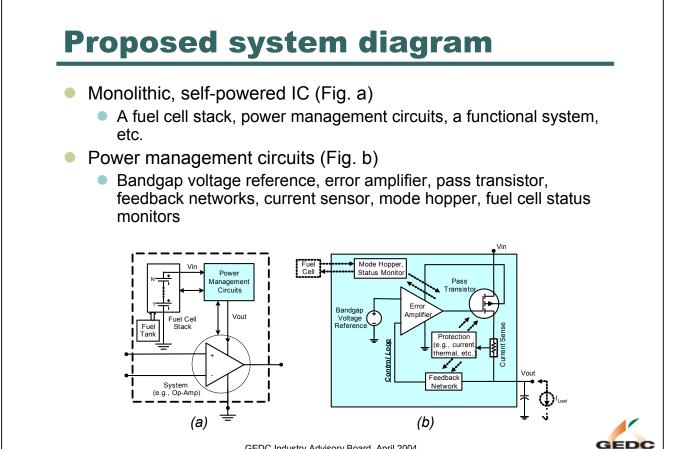
Integrated Micro-Fuel Cell

- Fabricate DMFC into micro-electromechanical systems (MEMS) through silicon CMOS processes
- Low power capability due to available current limitation
- Can be Integrated with IC on the same silicon wafer
- On-board Fuel (methanol) Storage
- Ambient Temperature
- Prof. Kohl and others from Chemical Engineering at Georgia Tech are investigating the integration of micro-fuel cell technology with silicon CMOS
 - Fabrication of microchannel with all-CMOS processes
 - Design and fabrication of integrated fuel reservoir with bidirectional microvalves
 - Design and evaluation of membranes and electrodes



Power Management Challenges

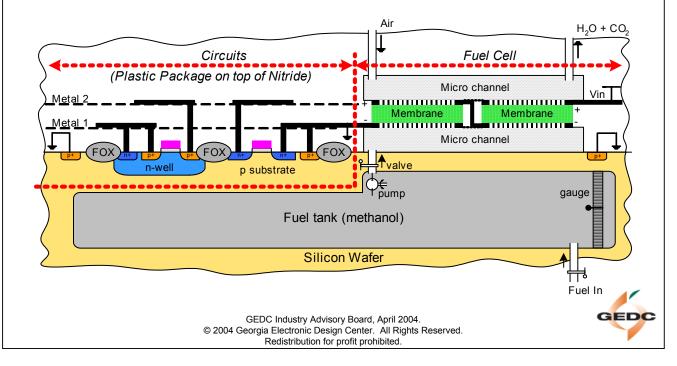




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Proposed Physical Profile View

- All CMOS processes
- Integrate fuel cell and circuits onto the same silicon wafer



Present status and Goal

Present Status:

Infancy (initial investigation and fund match)

Predominance:

Expertise in power IC design and access to the on-going research of Prof. Kohl at Georgia Tech (leading expert in integrated micro-fuel cell technology)

Objective:

To develop the technology necessary to support and manage integrated fuel cells (e.g., regulation, maintenance, interface, protection, etc.)

<u>Ultimate Goal:</u>

To develop a monolithic, self-powered IC, where the battery, its power management circuits, and the system are all integrated onto a single chip.

