

1

GEDC

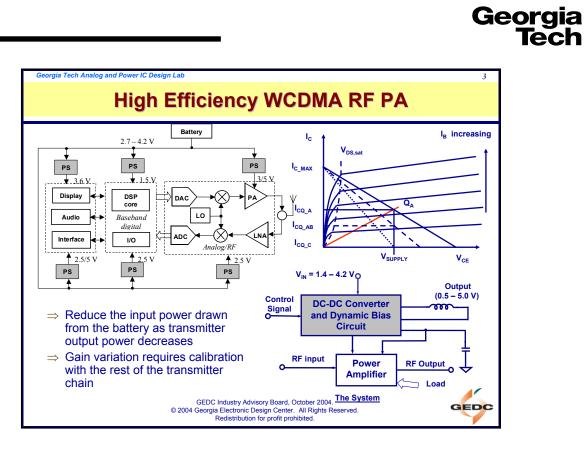


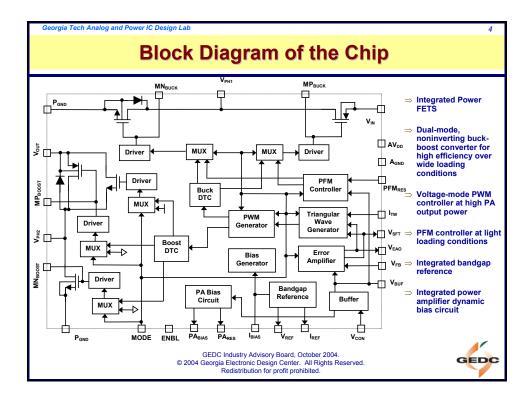
Advisor: Prof. Gabriel A. Rincón-Mora

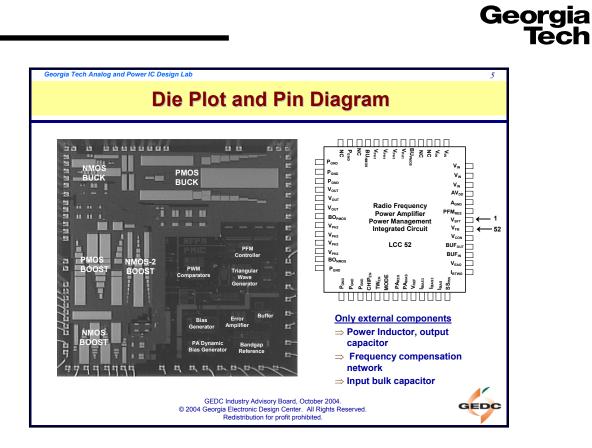
Georgia Tech Analog and Power IC Design Laboratory School of Electrical and Computer Engineering Georgia Institute of Technology

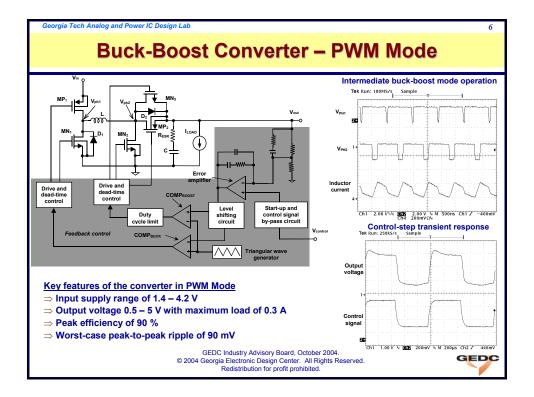
> GEDC Industry Advisory Board, October 2004. © 2004 Georgia Electronic Design Center. All Rights Reserved. Redistribution for profit prohibited.

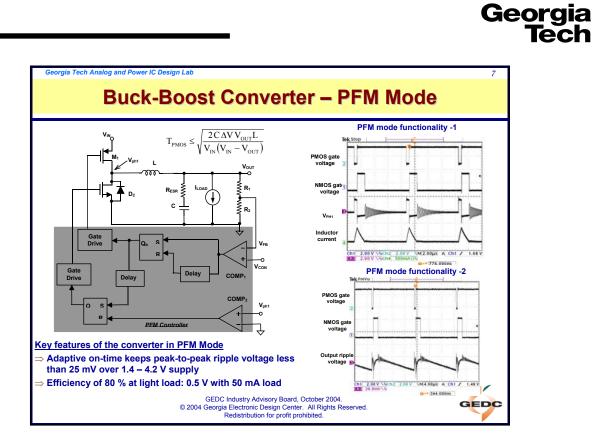
Georgia Tech Analog and Power IC Design Lab 2
Abstract
 Energy-efficient, linear RF power amplifiers are <i>critical</i> and <i>paramount</i> to achieve <i>longer battery life</i> in state-of-the-art wireless handsets.
 In the proposed system, the energy-efficiency of a WCDMA RF PA is improved by dynamically adjusting the supply voltage and current as a function of its transmitted power.
 Key Features of the Power Management IC Low voltage ⇒ Single cell operation (<i>Li-ion/NiCd/NiMH</i>) Integrated ⇒ Except filter inductor, capacitor and compensation High efficiency ⇒ Buck, Buck-boost, and Boost mode operation Low quiescent power ⇒ PFM Mode at light load for better standby performance Integrated dynamic gate(base) bias circuit
 1.96 GHz, 3.84 MHz HPSK Modulation WCDMA RF PA 25 dBm maximum output power Less than –35 dBc/-58 dBc adjacent/alternate channel leakage ratio (ACLR)
 Less than 10 % rms error vector magnitude (EVM) More than seven times average efficiency improvement over fixed-supply class- AB PA GEDC Industry Advisory Board, October 2004. © 2004 Georgia Electronic Design Center. All Rights Reserved. Redistribution for profit prohibited.

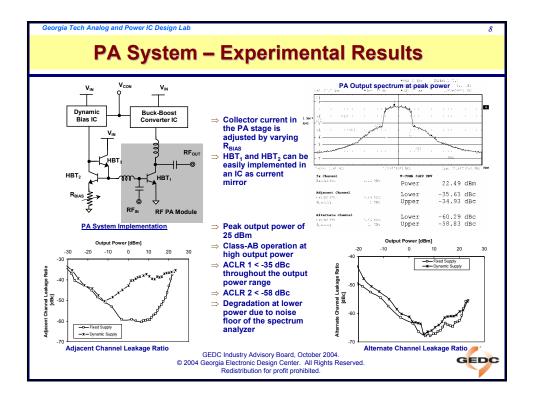


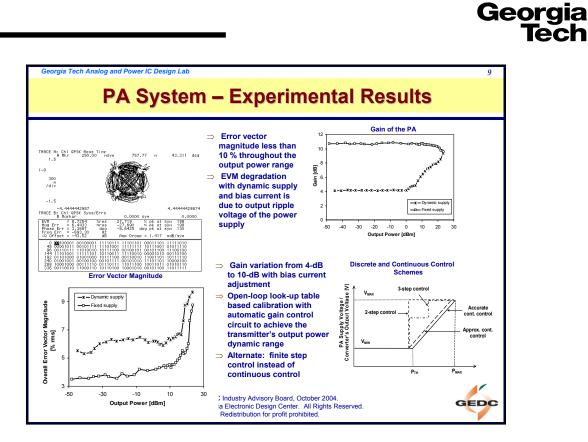


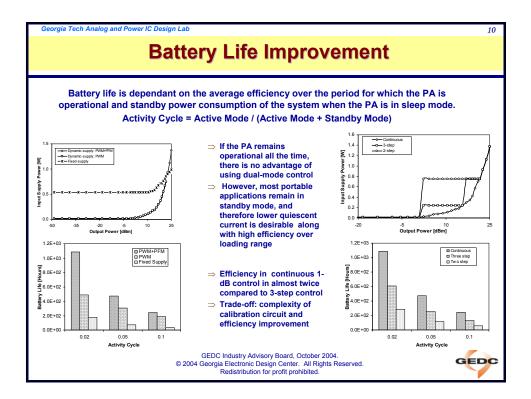


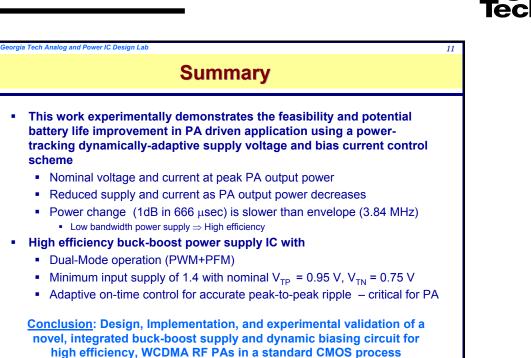












GEDC Industry Advisory Board, October 2004. © 2004 Georgia Electronic Design Center. All Rights Reserved. Redistribution for profit prohibited.



Georgi