

ECE 3055 Section B
Computer Architecture and Operating Systems
Fall 2006
COURSE SYLLABUS

Instructor: Prof. Hsien-Hsin "Sean" Lee

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Course Prerequisite: ECE 2031 Digital Design Lab

Teaching Assistants: To be announced.

Course Description: This course is designed to provide core concepts of computer architecture and operating systems including Instruction set architectures (ISA), compiler/ISA relationships, pipelined datapaths. Memory hierarchy, memory management, and protection. Processes, threads, CPU scheduling, synchronization, file system, and associated techniques.

Course Webs:

- <http://www.ece.gatech.edu/~leehs/ECE3055>
- <http://webct.gatech.edu> (For discussion and accessing your grades)

Class Meetings: MW 3:05-4:25pm Van Leer C241

Textbooks (Required):

- *Computer Organization and Design: The Hardware and Software Interface* by David A. Patterson and John L. Hennessy. Third Edition. Morgan-Kaufmann.
- *Operating System Concepts* by A. Siberschatz, P. Galvin, and G. Gagne. Sixth Edition. Wiley.

Course Slides and Schedule: Check out the course website.

Grading:

- 5 programming assignments. Individual work. No late turn-in accepted. (35% total)
- 3 in-class exams (15% each)
- Final exam (20%)

Honor Code: Students are expected to abide by the Georgia Tech Honor Code and to avoid any instances of academic misconduct in homework assignments and exams. Any violation will be immediately and directly reported to the Dean of Students' Affairs for further action.