## Homework Assignment No. 14-Solutions

## Problem 1-(10 points)

If the comparator used in Fig. 8.4-1 has a dominant pole at $10^{4}$ radians $/ \mathrm{sec}$ and a gain of $10^{3}$, how long does it take $C_{A Z}$ to charge to $99 \%$ of its final value, $V_{O S}$ ? What is the final value that the capacitor, $C_{A Z}$, will charge to if left in the configuration of Fig. 8.4-1(b) for a long time?

## Solution

The output voltage for the circuit shown can be expressed as,

$$
V_{\text {out }}(s)=\left(-V_{\text {OS }}-V_{\text {out }}(s)\right)\left(\frac{A_{v}(0)}{1+\frac{s}{\left|p_{1}\right|}}\right)
$$

This can be solved for the transfer $V_{\text {out }}(s) / V_{O S}$ as follows,

$$
\frac{V_{\text {out }}(s)}{V_{O S}(s)}=\frac{\frac{A_{v}(0)}{1+\frac{s}{\left|p_{1}\right|}}}{1+\frac{A_{v}(0)}{1+\frac{s}{\left|p_{1}\right|}}}=\frac{A_{v}(0)}{1+A_{v}(0)+\frac{s}{\left|p_{1}\right|}}=\frac{A_{v}(0)\left|p_{1}\right|}{s+\left(1+A_{v}(0)\right)\left|p_{1}\right|}
$$

Assuming $V_{O S}(s)$ is a step function then,

$$
V_{\text {out }}(s)=-\frac{V_{O S}}{s}\left(\frac{A_{v}(0)\left|p_{1}\right|}{s+\left(1+A_{v}(0)\right)\left|p_{1}\right|}\right)=-\frac{A_{v}(0) V_{O S}}{1+A_{\mathrm{V}}(0)}\left[\frac{1}{s}-\frac{1}{s+\left(1+A_{v}(0)\left|p_{1}\right|\right)}\right]
$$

Taking the inverse Laplace transform gives,

$$
v_{\text {out }}(t)=-\frac{A_{v}(0) V_{O S}}{1+A_{v}(0)}\left[1-e^{-\left[1+A_{v}(0)\right] \mid p 1 t}\right]
$$

Let $v_{\text {out }}(t)=-0.99 V_{\text {OS }}$ and solve for the time $T$.

$$
\begin{aligned}
& v_{\text {out }}(t)=-0.99 V_{\text {OS }}=-\frac{1000 V_{\text {OS }}}{1000+1}\left[1-e^{-1001 \cdot 10^{4} T}\right] \\
& 1-\frac{1001}{1000} \cdot \frac{99}{100}=0.0090=e^{-1001 \cdot 10^{4} T} \Rightarrow \quad 110.99=e^{1001 \cdot 10^{4} T} \\
\therefore \quad & T=0.9990 \times 10^{-7} \ln (110.99)=\underline{\underline{0.47 \mu \mathrm{~s}}} \\
\text { As } t \rightarrow & \infty, v_{\text {out }}(t) \rightarrow-\frac{1000 V_{\text {OS }}}{1000+1}=\underline{\underline{0.999}} \underline{\underline{O S}}
\end{aligned}
$$

## Problem 2-(10 points)

What is the gain and -3 dB bandwidth (in Hz ) of Fig. P8.6-3 if $C_{L}=1 \mathrm{pF}$ ? Ignore reverse bias voltage effects on the pn junctions and assume the bulk-source and bulk-drain areas are given by $W \times 5 \mu \mathrm{~m}$. The $W / L$ ratios for M 1 and M 2 are $10 \mu \mathrm{~m} / 1 \mu \mathrm{~m}$ and for the remaining PMOS transistors the $W / L$ ratios are all $2 \mu \mathrm{~m} / 1 \mu \mathrm{~m}$.


Fig. P8.6-3

## Solution

A small-signal model which can be used to solve this problem is shown.

The voltage gain and the -3 dB bandwidth can be expressed as,


$$
\frac{v_{\text {out }}}{v_{\text {in }}}=g_{m} R_{o} \quad \text { and } \quad \omega_{-3 \mathrm{~dB}}=\frac{1}{\left(C_{L}+0.5 C_{o}\right) 2 R_{o}}
$$

The various values in the above relationships are:

$$
\begin{aligned}
& g_{m}=\sqrt{2 \cdot K_{N}\left(W_{1} / L_{1}\right) I_{D 1}}=\sqrt{2 \cdot 110 \cdot 10 \cdot 25} \mu \mathrm{~S}=234.5 \mu \mathrm{~S} \\
& R_{o} \approx \frac{1}{g_{m 3}}\left\|r_{d s 1}\right\| r_{d s 3} \| r_{d s 5}, \quad g_{m 3}=\sqrt{2 \cdot K_{P}\left(W_{1} / L_{1}\right) I_{D 3}}=\sqrt{2 \cdot 50 \cdot 2 \cdot 5} \mu \mathrm{~S}=31.62 \mu \mathrm{~S} \\
& r_{d s 1}=\frac{1}{0.04 \cdot 25 \mu \mathrm{~A}}=1 \mathrm{M} \Omega, r_{d s 3}=\frac{1}{0.05 \cdot 5 \mu \mathrm{~A}}=4 \mathrm{M} \Omega \text { and } r_{d s 5}=\frac{1}{0.04 \cdot 20 \mu \mathrm{~A}}=0.8 \mathrm{M} \Omega \\
& \therefore R_{o}=31.623 \mathrm{k} \Omega\|1 \mathrm{M} \Omega\| 4 \mathrm{M} \Omega \| 0.8 \mathrm{M} \Omega=29.31 \mathrm{k} \Omega \\
& C_{o} \approx C_{g s 3}+C_{b d 1}+C_{b d 3}+C_{b d 5} \quad C_{g s 3}=C G S O \cdot W_{5}+0.67 \cdot C_{o x} \cdot W_{5} \cdot L_{5} \\
& =220 \times 10^{-12} \mathrm{~F} / \mathrm{m} \cdot 2 \times 10^{-6} \mathrm{~m}+0.67 \cdot 24.7 \times 10^{-4} \mathrm{~F} / \mathrm{m}^{2} .2 \times 10^{-12} \mathrm{~m}^{2}=3.73 \mathrm{fF} \\
& C_{b d 1}=C J \cdot A S+C J S W \cdot P S=770 \times 10^{-6} \mathrm{~F} / \mathrm{m}^{2} \cdot 50 \times 10^{-12} \mathrm{~m}^{2}+380 \times 10^{-12} \mathrm{~F} / \mathrm{m} \cdot 30 \times 10^{-6} \mathrm{~m} \\
& =38.5 \mathrm{fF}+11.4 \mathrm{fF}=49.9 \mathrm{fF} \\
& C_{b d 3}=C_{b d 5}=560 \times 10^{-6} \mathrm{~F} / \mathrm{m}^{2} \cdot 10 \times 10^{-12} \mathrm{~m}^{2}+350 \times 10^{-12} \mathrm{~F} / \mathrm{m} \cdot 14 \times 10^{-6} \mathrm{~m}=10.5 \mathrm{fF} \\
& \therefore \quad C_{o}=74.6 \mathrm{fF} \rightarrow \omega_{-3 \mathrm{~dB}}=\frac{1}{(1.073 \mathrm{pF}) 58.62 \mathrm{k} \Omega}=16.445 \times 10^{6} \mathrm{rads} / \mathrm{sec}
\end{aligned}
$$

Finally, $\quad f_{-3 \mathrm{~dB}}=\underline{\underline{2.62 M H z}} \quad$ and $A_{v}=\underline{\underline{6.873 V} / \mathrm{V}}$

## Problem 3-(10 points)

Assume that a comparator consists of an amplifier cascaded with a latch. Assume the amplifier has a gain of $5 \mathrm{~V} / \mathrm{V}$ and a -3 dB bandwidth of $1 / \tau_{L}$, where $\tau_{L}$ is the latch time constant and is equal to 10 ns . Find the propagation time delay for the overall configuration if the applied input voltage is $0.05\left(V_{O H}{ }^{-} V_{O L}\right)$ and the voltage applied to the latch from the amplifier is (a) $\Delta V_{i}=0.05\left(V_{O H^{-}} V_{O L}\right)$, (b) $\Delta V_{i}=0.1\left(V_{O H^{-}} V_{O L}\right)$, (c) $\Delta V_{i}=0.15\left(V_{O H^{-}}\right.$ $\left.V_{O L}\right)$ and (d) $\Delta V_{i}=0.2\left(V_{O H}-V_{O L}\right)$. Assume that the latch is enabled as soon as the output of the amplifier is equal to $0.05\left(V_{O H}-V_{O L}\right)$. From your results, what value of $\Delta V_{i}$ would give minimum propagation time delay?

## Solution

The transfer function of the amplifier is $\quad A_{v}(s)=\frac{A_{v}(0)}{s \tau_{L}+1}$
The output voltage of the amplifier is $v_{o}(t)=A_{v}(0)\left[1-e^{-t / \tau_{L}}\right] \Delta V_{i}$
Let $\Delta V_{i}=x \cdot\left(V_{O H^{-}} V_{O L}\right)$, therefore the delay of the amplifier can be found as

$$
x\left(V_{O H^{-}} V_{O L}\right)=A_{v}(0)\left[1-e^{-t 1} / \tau_{L}\right] 0.05\left(V_{O H^{-}} V_{O L}\right)=5\left[1-e^{-t_{1} / \tau_{L}}\right] 0.05\left(V_{O H^{-}} V_{O L}\right)
$$

or

$$
x=0.25\left[1-e^{-t_{1} / \tau_{L}}\right] \quad \rightarrow \quad t_{1}=\tau_{L} \ln \left(\frac{1}{1-4 x}\right)
$$

The delay of the latch can be found as

$$
t_{2}=\tau_{L} \ln \left(\frac{V_{O H^{-}} V_{O L}}{2 x\left(V_{O H^{-}} V_{O L}\right)}\right)=\tau_{L} \ln \left(\frac{1}{2 x}\right)
$$

The propagation time delay of the comparator can be expressed in terms of $x$ as,

$$
t_{p}=t_{1}+t_{2}=\tau_{L} \ln \left(\frac{1}{1-4 x}\right)+\tau_{L} \ln \left(\frac{1}{2 x}\right)=\tau_{L} \ln \left(\frac{1}{2 x-8 x^{2}}\right)
$$

Thus,

$$
\begin{array}{lll}
x=0.05=1 / 20 & \Rightarrow & \tau_{p}=t_{1}+t_{2}=2.23 \mathrm{~ns}+2.30 \mathrm{~ns}=\underline{\underline{25.26 \mathrm{~ns}}} \\
x=0.1=1 / 10 & \Rightarrow & \tau_{p}=t_{1}+t_{2}=5.11 \mathrm{~ns}+16.09 \mathrm{~ns}=\underline{\underline{21.20 \mathrm{~ns}}} \\
x=0.15 & \Rightarrow & \tau_{p}=t_{1}+t_{2}=9.16 \mathrm{~ns}+12.04 \mathrm{~ns}=\underline{\underline{21.20 \mathrm{~ns}}} \\
x=0.2=1 / 5 & \Rightarrow & \tau_{p}=t_{1}+t_{2}=16.09 \mathrm{~ns}+9.16 \mathrm{~ns}=\underline{\underline{25.26 \mathrm{~ns}}}
\end{array}
$$

Note that differentiating $t_{p}$ with respect to $x$ and setting to zero gives

$$
x_{\min }=1 / 8=0.125
$$

Therefore, minimum delay of $\underline{\underline{20.08 \mathrm{~ns}}}$ is achieved when $\underline{\underline{x=1 / 8}}$.

## Problem 4-(10 points)

A comparator consists of an amplifier cascaded with a latch as shown below. The amplifier has a voltage gain of $10 \mathrm{~V} / \mathrm{V}$ and $f_{-3 \mathrm{~dB}}=100 \mathrm{MHz}$ and the latch has a time constant of 10 ns . The maximum and minimum voltage swings of the amplifier and latch are $V_{O H}$ and $V_{O L}$. When should the latch be enabled after the application of a step input to the amplifier of $0.05\left(V_{O H}-V_{O L}\right)$ to get minimum overall propagation time delay? What is the value of the minimum propagation time delay? It may useful to recall that the propagation time delay of the latch is given as $t_{p}=\tau_{L} \ln \left(\frac{V_{O H^{-}} V_{O L}}{2 v_{i l}}\right)$ where $v_{i l}$ is the latch input ( $\Delta V_{i}$ of the text).


## Solution

The solution is based on the figure shown.
We note that,

$$
v_{o a}(t)=10\left[1-e^{-\omega-3 \mathrm{~dB} t}\right] 0.05\left(V_{O H^{-}} V_{O L}\right)
$$

If we define the input voltage to the latch as,

$$
v_{i l}=x \cdot\left(V_{O H}-V_{O L}\right)
$$

then we can solve for $t_{1}$ and $t_{2}$ as follows:

$$
x \cdot\left(V_{O H^{-}} V_{O L}\right)=10\left[1-e^{-\omega-3 \mathrm{~dB} t 1}\right] 0.05\left(V_{O H^{-}}\right.
$$

$\left.V_{O L}\right) \rightarrow x=0.5\left[1-e^{-\omega-3 \mathrm{~dB} t_{1}}\right]$


This gives,
S01E3S1

$$
t_{1}=\frac{1}{\omega_{-3 \mathrm{~dB}}} \ln \left(\frac{1}{1-2 x}\right)
$$

From the propagation time delay of the latch we get,

$$
\begin{aligned}
& t_{2}=\tau_{L} \ln \left(\frac{V_{O H^{-}} V_{O L}}{2 v_{i l}}\right)=\tau_{L} \ln \left(\frac{1}{2 x}\right) \\
& \therefore \quad t_{p}=t_{1}+t_{2}=\frac{1}{\omega_{-3 \mathrm{~dB}}} \ln \left(\frac{1}{1-2 x}\right)+\tau_{L} \ln \left(\frac{1}{2 x}\right) \rightarrow \frac{d t_{p}}{d x}=0 \text { gives } x=\frac{\pi}{1+2 \pi}=0.4313 \\
& t_{1}=\frac{10 \mathrm{~ns}}{2 \pi} \ln (1+2 \pi)=1.592 \mathrm{~ns} \cdot 1.9856=\underline{\underline{3.16 n s}} \text { and } t_{2}=10 \mathrm{~ns} \ln \left(\frac{1+2 \pi}{2 \pi}\right)= \\
& 1.477 \mathrm{~ns}
\end{aligned}
$$

