Homework Assignment No. 14 - Solutions

Problem 1 - (10 points)

If the comparator used in Fig. 8.4-1 has a dominant pole at 10^4 radians/sec and a gain of 10^3 , how long does it take C_{AZ} to charge to 99% of its final value, V_{OS} ? What is the final value that the capacitor, C_{AZ} , will charge to if left in the configuration of Fig. 8.4-1(b) for a long time?

<u>Solution</u>

...

The output voltage for the circuit shown can be expressed as,

$$V_{out}(s) = (-V_{OS} - V_{out}(s)) \left(\frac{A_v(0)}{1 + \frac{s}{|p_1|}}\right)$$

This can be solved for the transfer $V_{out}(s)/V_{OS}$ as follows,

$$\frac{V_{out}(s)}{V_{OS}(s)} = \frac{\frac{A_v(0)}{1 + \frac{s}{|p_1|}}}{1 + \frac{A_v(0)}{1 + \frac{s}{|p_1|}}} = \frac{A_v(0)}{1 + A_v(0) + \frac{s}{|p_1|}} = \frac{A_v(0)|p_1|}{s + (1 + A_v(0))|p_1|}$$

Assuming $V_{OS}(s)$ is a step function then,

$$V_{out}(s) = -\frac{V_{OS}}{s} \left(\frac{A_v(0)|p_1|}{s + (1 + A_v(0)) |p_1|} \right) = -\frac{A_v(0)V_{OS}}{1 + A_v(0)} \left[\frac{1}{s} - \frac{1}{s + (1 + A_v(0)|p_1|)} \right]$$

Taking the inverse Laplace transform gives,

$$v_{out}(t) = -\frac{A_v(0)V_{OS}}{1 + A_v(0)} \left[1 - e^{-[1 + A_v(0)]|p_1|t}\right]$$

Let $v_{out}(t) = -0.99 V_{OS}$ and solve for the time T.

$$v_{out}(t) = -0.99V_{OS} = -\frac{1000V_{OS}}{1000+1} [1 - e^{-1001 \cdot 10^4 T}]$$

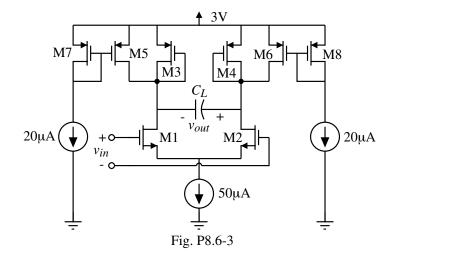
$$1 - \frac{1001}{1000} \cdot \frac{99}{100} = 0.0090 = e^{-1001 \cdot 10^4 T} \implies 110.99 = e^{1001 \cdot 10^4 T}$$

$$T = 0.9990 \times 10^{-7} \ln(110.99) = 0.47 \mu s$$

As
$$t \to \infty$$
, $v_{out}(t) \to -\frac{1000V_{OS}}{1000+1} = \underline{0.999V}_{OS}$

Problem 2 - (10 points)

What is the gain and -3dB bandwidth (in Hz) of Fig. P8.6-3 if $C_L = 1$ pF? Ignore reverse bias voltage effects on the pn junctions and assume the bulk-source and bulk-drain areas are given by $W \times 5\mu$ m. The *W/L* ratios for M1 and M2 are 10 μ m/1 μ m and for the remaining PMOS transistors the *W/L* ratios are all 2 μ m/1 μ m.



<u>Solution</u>

A small-signal model which + can be used to solve this C_L problem is shown. 8m^Vin 8m^Vin R_o R 2 2 The voltage gain and the -3dB bandwidth can be S8.6-3 expressed as, $\frac{v_{out}}{v_{in}} = g_m R_o$ and $\omega_{-3dB} = \frac{1}{(C_L + 0.5C_o)2R_o}$

$$g_{m} = \sqrt{2 \cdot K_{N}(W_{1}/L_{1})I_{D1}} = \sqrt{2 \cdot 110 \cdot 10 \cdot 25} \ \mu\text{S} = 234.5\mu\text{S}$$

$$R_{o} \approx \frac{1}{g_{m3}} \|r_{ds1}\|r_{ds3}\|r_{ds5}, \ g_{m3} = \sqrt{2 \cdot K_{P}(W_{1}/L_{1})I_{D3}} = \sqrt{2 \cdot 50 \cdot 2 \cdot 5} \ \mu\text{S} = 31.62\mu\text{S}$$

$$r_{ds1} = \frac{1}{0.04 \cdot 25\mu\text{A}} = 1\text{M}\Omega, \ r_{ds3} = \frac{1}{0.05 \cdot 5\mu\text{A}} = 4\text{M}\Omega \text{ and } r_{ds5} = \frac{1}{0.04 \cdot 20\mu\text{A}} = 0.8\text{M}\Omega$$

$$\therefore R_{o} = 31.623\text{k}\Omega\|1\text{M}\Omega\|4\text{M}\Omega\|0.8\text{M}\Omega = 29.31\text{k}\Omega$$

$$C_{o} \approx C_{gs3} + C_{bd1} + C_{bd3} + C_{bd5} \qquad C_{gs3} = CGSO \cdot W_{5} + 0.67 \cdot C_{ox} \cdot W_{5} \cdot L_{5}$$

$$= 220 \times 10^{-12}\text{F/m} \cdot 2 \times 10^{-6}\text{m} + 0.67 \cdot 24.7 \times 10^{-4}\text{F/m}^{2} \cdot 2 \times 10^{-12}\text{m}^{2} = 3.73\text{fF}$$

$$C_{bd1} = CJ \cdot AS + CJSW \cdot PS = 770 \times 10^{-6}\text{F/m}^{2} \cdot 50 \times 10^{-12}\text{m}^{2} + 380 \times 10^{-12}\text{F/m} \cdot 30 \times 10^{-6}\text{m}$$

$$= 38.5\text{fF} + 11.4\text{fF} = 49.9\text{fF}$$

$$C_{bd3} = C_{bd5} = 560 \times 10^{-6}\text{F/m}^{2} \cdot 10 \times 10^{-12}\text{m}^{2} + 350 \times 10^{-12}\text{F/m} \cdot 14 \times 10^{-6}\text{m} = 10.5\text{fF}$$

$$\therefore C_{o} = 74.6\text{fF} \rightarrow \omega_{-3\text{dB}} = \frac{1}{(1.073\text{pF})58.62\text{k}\Omega} = 16.445 \times 10^{6}\text{rads/sec}$$
Finally,
$$f_{-3\text{dB}} = \underline{2.62\text{MHz}} \text{ and } A_{v} = \underline{6.873\text{V/V}}$$

Problem 3 – (10 points)

Assume that a comparator consists of an amplifier cascaded with a latch. Assume the amplifier has a gain of 5V/V and a -3dB bandwidth of $1/\tau_L$, where τ_L is the latch time constant and is equal to 10ns. Find the propagation time delay for the overall configuration if the applied input voltage is $0.05(V_{OH}-V_{OL})$ and the voltage applied to the latch from the amplifier is (a) $\Delta V_i = 0.05(V_{OH}-V_{OL})$, (b) $\Delta V_i = 0.1(V_{OH}-V_{OL})$, (c) $\Delta V_i = 0.15(V_{OH}-V_{OL})$ and (d) $\Delta V_i = 0.2(V_{OH}-V_{OL})$. Assume that the latch is enabled as soon as the output of the amplifier is equal to $0.05(V_{OH}-V_{OL})$. From your results, what value of ΔV_i would give minimum propagation time delay?

<u>Solution</u>

The transfer function of the amplifier is $A_v(s) = \frac{A_v(0)}{s\tau_I + 1}$

The output voltage of the amplifier is $v_o(t) = A_v(0)[1-e^{-t/\tau L}]\Delta V_i$

Let $\Delta V_i = x \cdot (V_{OH} - V_{OL})$, therefore the delay of the amplifier can be found as

$$x(V_{OH}-V_{OL}) = A_v(0)[1-e^{-t_1/\tau_L}]0.05(V_{OH}-V_{OL}) = 5[1-e^{-t_1/\tau_L}]0.05(V_{OH}-V_{OL})$$

or

$$x = 0.25[1 - e^{-t_1/\tau_L}] \quad \rightarrow \qquad t_1 = \tau_L \ln\left(\frac{1}{1 - 4x}\right)$$

The delay of the latch can be found as

$$t_2 = \tau_L \ln\left(\frac{V_{OH} - V_{OL}}{2x(V_{OH} - V_{OL})}\right) = \tau_L \ln\left(\frac{1}{2x}\right)$$

The propagation time delay of the comparator can be expressed in terms of x as,

$$t_p = t_1 + t_2 = \tau_L \ln\left(\frac{1}{1 - 4x}\right) + \tau_L \ln\left(\frac{1}{2x}\right) = \tau_L \ln\left(\frac{1}{2x - 8x^2}\right)$$

Thus,

\Rightarrow	$\tau_p = t_1 + t_2 = 2.23$ ns+2.30ns = 25.26 ns
⇒	$\tau_p = t_1 + t_2 = 5.11$ ns+16.09ns = <u>21.20ns</u>
⇒	$\tau_p = t_1 + t_2 = 9.16$ ns+12.04ns = <u>21.20ns</u>
⇒	$\tau_p = t_1 + t_2 = 16.09$ ns+9.16ns = 25.26 ns
	⇒ ⇒

Note that differentiating t_p with respect to x and setting to zero gives

$$x_{min} = 1/8 = 0.125$$

Therefore, minimum delay of 20.08 is achieved when x = 1/8.

Problem 4 - (10 points)

A comparator consists of an amplifier cascaded with a latch as shown below. The amplifier has a voltage gain of 10V/V and $f_{-3dB} = 100$ MHz and the latch has a time constant of 10ns. The maximum and minimum voltage swings of the amplifier and latch are V_{OH} and V_{OL} . When should the latch be enabled after the application of a step input to the amplifier of $0.05(V_{OH}-V_{OL})$ to get minimum overall propagation time delay? What is the value of the minimum propagation time delay? It may useful to recall that the propagation time delay of

the latch is given as $t_p = \tau_L \ln\left(\frac{V_{OH} - V_{OL}}{2v_{il}}\right)$ where v_{il} is the latch input (ΔV_i of the text).

$$v_{in} = 0.05(V_{OH}-V_{OL})$$

$$Amplifier$$

$$A_{\nu}(0)=10V/V$$

$$f_{-3dB}=100MHz$$

$$v_{il}$$

$$Latch$$

$$\tau_{L}=10ns$$

$$Comparator$$

$$Enable$$

$$S01E3P1$$

Solution

The solution is based on the figure shown. We note that,

 $v_{oa}(t) = 10[1 - e^{-\omega - 3 dBt}]0.05(V_{OH} - V_{OL}).$ If we define the input voltage to the latch as, $v_{il} = x \cdot (V_{OH} - V_{OL})$

then we can solve for t_1 and t_2 as follows:

$$\begin{split} x \cdot (V_{OH} - V_{OL}) &= 10[1 - e^{-\omega_{-} 3 \mathrm{dB} t_{1}}] 0.05 (V_{OH} - V_{OL}) & \rightarrow x = 0.5 [1 - e^{-\omega_{-} 3 \mathrm{dB} t_{1}}] \end{split}$$

This gives,

$$t_1 = \frac{1}{\omega_{-3\mathrm{dB}}} \ln\left(\frac{1}{1-2x}\right)$$

From the propagation time delay of the latch we get,

$$t_{2} = \tau_{L} \ln\left(\frac{V_{OH} - V_{OL}}{2v_{il}}\right) = \tau_{L} \ln\left(\frac{1}{2x}\right)$$

$$\therefore \qquad t_{p} = t_{1} + t_{2} = \frac{1}{\omega_{-3dB}} \ln\left(\frac{1}{1 - 2x}\right) + \tau_{L} \ln\left(\frac{1}{2x}\right) \implies \frac{dt_{p}}{dx} = 0 \text{ gives } x = \frac{\pi}{1 + 2\pi} = 0.4313$$

$$t_{1} = \frac{10\text{ns}}{2\pi} \ln\left(1 + 2\pi\right) = 1.592\text{ns} \cdot 1.9856 = \underline{3.16\text{ns}} \text{ and } t_{2} = 10\text{ns} \ln\left(\frac{1 + 2\pi}{2\pi}\right) = 1.477\text{ns}$$

1.477ns

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