
References

1. J. Bhaskar, *A VHDL Primer*. Englewood Cliffs, NJ: Prentice Hall, 1995.
 2. B. Cohen, *VHDL Coding Styles and Methodologies*. Boston, MA: Kluwer Academic, 1995.
 3. D. Gajski and R. H. Kuhn, "Guest Editors Introduction—New VLSI Tools," *IEEE Computer*, vol. 16, no.2, 1983, pp. 14–17.
 4. J. Hayes, *Introduction to Digital Logic*. Reading, MA: Addison-Wesley, 1993
 5. *IEEE Standard VHDL Language Reference Manual: ANSI/IEEE Std 1076–1993*. New York : IEEE, June 1994.
 6. R. Lipsett, C. Schaefer, and C. Ussery, *VHDL: Hardware Description and Design*. Boston : Kluwer Academic, 1989.
 7. V. K. Madisetti, "Rapid Digital System Prototyping: Current Practice and Future Challenges," *IEEE Design and Test*, Fall 1996, pp. 12–22.
 8. V. K. Madisetti and T. W. Egolf, "Virtual Prototyping of Embedded Microcontroller-Based DSP Systems," *IEEE Micro*, pp. 9–21, 1995.
 9. D. Patterson and J. Hennessey, *Computer Organization & Design: The Hardware/Software Interface*. San Francisco : Morgan Kaufmann, 1994.
 10. D. Perry, *VHDL*. New York, NY: McGraw Hill, Second Edition, 1994.
 11. M. Richards, "The Rapid Prototyping of Application-Specific Signal Processors Program," *Proceedings of the First Annual RASSP Conference*, Defense Advanced Research Projects Agency, 1994.
-

12. K. Skahill, *VHDL for Programmable Logic*. Reading, MA: Addison-Wesley, 1996.
 13. D. E. Thomas, C. Y. Hitchcock III, T. J. Kowalski, J. V. Rajan, and R. A. Walker, "Automatic Data Path Synthesis," *IEEE Computer*, vol. 16, no. 12, December 1983, pp. 59–70.
 14. R. Walker and D. E. Thomas, "A Model of Design Representation and Synthesis," *Proceedings of the 22nd ACM/IEEE Design Automation Conference*, 1985 pp. 453–459.
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