## Homework Assignment No. 3 - Solutions

## Problem 1-(10 points)

Assume an LPLL has $F(s)=1$ and the PLL parameters are $K_{d}=0.8 \mathrm{~V} /$ radians, $K_{o}=100 \mathrm{MHz} / \mathrm{V}$, and the oscillation frequency, $f_{\text {osc }}=500 \mathrm{MHz}$. Sketch the control voltage at the output of the phase detector if the input frequency jumps from 500 MHz to 650 MHz .

## Solution

Find the transfer function from the input frequency, $f_{i n}$, to the output of the phase detector, $v_{d}$.

$$
\begin{aligned}
& V_{d}=K_{d}\left(\theta_{1^{-}} \theta_{2}\right)=K_{d^{-}} \theta_{1^{-}} \frac{K_{d} K_{o}}{s} V_{d} \\
& V_{d}\left(1+\frac{K_{d} K_{o}}{s}\right)=K_{d} \theta_{1}=K_{d}\left(\frac{\omega_{1}}{s}\right)
\end{aligned}
$$

$$
\therefore \quad \frac{V_{d}}{\omega_{1}}=\frac{K_{d}}{s+K_{d} K_{o}} \rightarrow \quad V_{d}(\mathrm{~s})=\frac{K_{d}}{s+K_{d} K_{o}} \omega_{1}(s)=\frac{K_{d}}{s+K_{d} K_{o}} \frac{\Delta \omega_{1}}{s}=\frac{k_{1}}{s}+\frac{k_{2}}{s+K_{d} K_{o}}
$$

By partial fraction expansion we can show that $k_{1}=-k_{2}=\frac{K_{d} \Delta \omega_{1}}{K_{d} K_{o}}=\frac{K_{d} \Delta \omega_{1}}{K_{v}}=1.5 \mathrm{~V}$
Note the units of $\frac{K_{d} \Delta \omega_{1}}{K_{v}}$ are $\frac{(\mathrm{V} / \mathrm{rad})(\mathrm{rad} / \mathrm{sec})}{1 / \mathrm{sec}}=\mathrm{V}$
and $K_{v}=(2 \pi \cdot 100 \mathrm{MHz} / \mathrm{V})(0.8 \mathrm{~V} / \mathrm{rad})=.502.65 \times 10^{6}(1 / \mathrm{sec}$.
$\therefore \quad v_{d}(t)=\frac{K_{d} \Delta \omega_{1}}{K_{v}}\left(1-e^{-K_{v} t}\right)=1.5\left(1-e^{-502.65 \times 10^{6} t}\right)$
A plot of $v_{d}(t)$ is shown below.


## Problem 2-(10 points)

A Type I PLL incorporates a VCO with $K_{o}=100 \mathrm{MHz} / \mathrm{V}$, a phase detector with $K_{d}=1 \mathrm{~V} / \mathrm{rad}$, and a first-order, lowpass filter with $\omega_{L P F}=2 \pi \times 10^{6}$ radians/s shown below. A divider of 100 has been placed in the feedback path to implement a frequency synthesizer. (a.) Find the value of the natural damping frequency, $\omega_{n}$, and the damping factor, $\zeta$, for the transfer function $\phi_{\text {out }}(s) / \phi_{\text {in }}(s)$, for this PLL. (b.) If a step input of $\Delta \phi_{\text {in }}$ is applied at $t=0$, what is the steady-state phase error at the output of the phase detector, $\phi_{e}$ ? The steady-state error is evaluated by multiplying the desired phase by $s$ and letting $s \rightarrow 0$.


## Solution

(a.) $\phi_{\text {out }}=\frac{K_{o}}{s}\left(\frac{1}{\frac{s}{\omega_{L P F}}+1}\right) K_{d}\left(\phi_{\text {in }}-\frac{\phi_{\text {out }}}{N}\right) \rightarrow \phi_{\text {out }}\left[1+\frac{K_{o}}{s N}\left(\frac{K_{d}}{1+\frac{s}{\omega_{L P F}}}\right)\right]=\frac{K_{o}}{s}\left(\frac{K_{d}}{\frac{s}{\omega_{L P F}}+1}\right) \phi_{\text {in }}$
$\therefore \frac{\phi_{\text {out }}(s)}{\phi_{\text {in }}(s)}=\frac{K_{o} K_{d}}{s\left(1+\frac{s}{\omega_{L P F}}\right)+\frac{K_{o} K_{d}}{N}}=\frac{K_{o} K_{d} \omega_{L P F}}{s^{2}+\omega_{L P F} s+\frac{K_{o} K_{d} \omega_{L P F}}{N}}=\frac{\omega_{n}{ }^{2}}{s^{2}+2 \zeta \omega_{n} s+\omega_{n}^{2}}$
Thus, $\omega_{n}^{2}=\frac{K_{o} K_{d} \omega_{L P F}}{N}=\frac{2 \pi \times 10^{6} \cdot 2 \pi \times 10^{8}}{100}=4 \pi^{2} \times 10^{12} \rightarrow \omega_{n}=2 \pi \times 10^{6}$

$$
\begin{array}{ll} 
& \zeta=\frac{\omega_{L P F}}{2 \omega_{n}}=\frac{\omega_{L P F}}{2 \sqrt{\frac{K_{o} K_{d} \omega_{L P F}}{N}}}=\frac{1}{2} \sqrt{\frac{N \omega_{L P F}}{K_{o} K_{d}}}=\frac{1}{2} \sqrt{\frac{100 \cdot 2 \pi \times 10^{6}}{1 \cdot 2 \pi \times 10^{8}}}=0.5 \\
\therefore \quad & \omega_{n}=2 \pi \times 10^{6} \text { and } \zeta=0.5
\end{array}
$$

(b.) First we must solve for $\phi_{e}(s)$ which is found as

$$
\phi_{e}(s)=\frac{s\left(1+\frac{s}{\omega_{L P F}}\right)}{K_{o}} \phi_{\text {out }}(s)=\frac{s\left(1+\frac{s}{\omega_{L P F}}\right)}{K_{o}} \frac{K_{o} K_{d} \omega_{L P F}}{s^{2}+\omega_{L P F} s+\frac{K_{o} K_{d} \omega_{L P F}}{N}} \phi_{\text {in }}(s)
$$

If $\phi_{i n}(s)=\frac{\Delta \phi_{i n}}{s}$, then we can write $s \phi_{e}(s)=\frac{K_{d}\left(s^{2}+\omega_{L P F} s\right) \Delta \phi_{i n}}{s^{2}+\omega_{L P F} s+\frac{K_{o} K_{d} \omega_{L P F}}{N}}$
Therefore, we see that the steady-state error is $\phi(t=\infty)=0$.

## Problem 3-(10 points)

Modify the active filter shown of Problem 4 of Homework 2 to design the lag-lead loop filter shown below. The capacitors can be no larger than 10 pF . Give the values of $R_{1}, R_{2}, C_{1}$ and $C_{2}$.


## Solution

The transfer function corresponding to the above Bode plot is,

$$
F(s)=\frac{\frac{s}{10^{5}}+1}{\frac{1}{10^{4}}+1}
$$

The modification of the filter is shown where from Prob. 4 of Homework 2,

$$
R_{T i}=\frac{2 R R_{i}+R^{2}}{R_{i}}
$$

The transfer function of this
 filter is found as,

$$
F(s)=\frac{V_{c}(s)}{V_{d}(s)}=\left(\frac{R_{T 2}}{R_{T 1}}\right) \frac{s R_{T 1} C_{1}+1}{s R_{T 2} C_{2}+1} \Rightarrow R_{T 2}=R_{T 1}=R_{T}, R_{T} C_{1}=10^{-5} \text { and } R_{T} C_{2}=10^{-4}
$$

We see if $R_{T 2}=R_{T 1}$, then $C_{2}=10 C_{1}$. Choosing $C_{2}=10 \mathrm{pF}$ gives $C_{1}=1 \mathrm{pF}$. This gives

$$
\begin{aligned}
& R_{T}=\frac{10^{-4}}{C_{2}}=\frac{10^{-4}}{10^{-11}}=10^{7} \\
& R_{T}=\frac{2 R R_{i}+R^{2}}{R_{i}}=2 R+\frac{R^{2}}{R_{1}}=20 \times 10^{3}+\frac{100 \times 10^{6}}{R_{1}}=10^{7} \Rightarrow R_{1}=\frac{100 \times 10^{6}}{10^{7}-20 \times 10^{3}}=10.02 \Omega
\end{aligned}
$$

Therefore, $R_{1}=R_{2}=\underline{\underline{10.02 \Omega}}, C_{1}=\underline{\underline{1 p F}}$ and $C_{2}=\underline{\underline{10 \mathrm{pF}}}$
The realization is completed by replacing each of the $R_{T}$ resistors with the following equivalent:


## Problem 4-(10 points)

Using the filter of Problem 3, find the value of $\omega_{n}$ and $\zeta$ of the PLL if $K_{d}=1 \mathrm{~V} /$ radians, $K_{o}=$ 2Mradians/V•sec. What is the steady state phase error in degrees if a frequency ramp of $10^{9}$ radians $/ \mathrm{sec} .^{2}$ is applied to the PLL?

## Solution

Using the definition give in the notes for the time constants of the passive lag-lead filter we get,

$$
\begin{aligned}
& F(s)=\frac{\frac{s}{10^{5}}+1}{\frac{1}{10^{4}}+1}=\frac{s \tau_{2}+1}{s\left(\tau_{1}+\tau_{2}\right)+1} \Rightarrow \tau_{2}=10^{-5} \mathrm{sec} . \text { and } \tau_{1}=9 \times 10^{-5} \mathrm{sec} . \\
\therefore \quad & \omega_{n}=\sqrt{\frac{K_{o} K_{d}}{\tau_{1}+\tau_{2}}}=\sqrt{\frac{2 \times 10^{6}}{10^{-4}}}=\sqrt{2} \times 10^{5}=\underline{\underline{141.4 \times 10^{3} \mathrm{radians} / \mathrm{sec} .}} \\
& \zeta=\frac{\omega_{n}}{2}\left(\tau_{2}+\frac{1}{K_{o} K_{d}}\right)=\frac{\sqrt{2} \times 10^{5}}{2}\left(10^{-5}+\frac{1}{2 \times 10^{6}}\right)=\frac{1}{\sqrt{2}}\left(1+\frac{1}{20}\right)=\underline{\underline{0.742}}
\end{aligned}
$$

Assuming the PLL has a high loop gain, then the steady-state phase error can be found as

$$
\theta_{e}(\infty)=\frac{\Delta \dot{\omega}}{\omega_{n}^{2}}=\frac{10^{9}}{2 \times 10^{10}}=\frac{1}{20} \text { radians }=\underline{\underline{2.86^{\circ}}}
$$

## Problem 5-(10 points)

Solve for the crossover frequency of the PLL of Problems 3 and 4 and find the phase margin. Use SPICE to find the open-loop frequency response of the PLL and from your plot determine the crossover frequency and phase margin and compare with your calculated values.

## Solution

The crossover frequency can be found as,

$$
\begin{aligned}
\omega_{c} & =\omega_{n} \sqrt{2 \zeta^{2}+\sqrt{4 \zeta^{4}+1}}=\sqrt{2} \times 10^{5} \sqrt{2 \cdot 0.742^{2}+\sqrt{4 \cdot 0.742^{4}+1}} \\
& =\sqrt{2} \times 10^{5}(1.6089)=2.275 \times 10^{5} \text { radians } / \mathrm{sec} .=36.208 \mathrm{kHz}
\end{aligned}
$$

The open loop transfer function is given as
$\mathrm{LG}(s)=\frac{K_{v}}{s}\left(\frac{1+s \tau_{1}}{1+s \tau_{2}}\right)=\frac{\sqrt{2} \times 10^{5}}{s}\left(\frac{1+s 10^{-5}}{1+s 10^{-4}}\right)$
The phase margin can be written as,

$$
\mathrm{PM}=180^{\circ}-90^{\circ}+\tan ^{-1}\left(\frac{\omega_{c}}{10^{5}}\right)-\tan ^{-1}\left(\frac{\omega_{c}}{10^{4}}\right)=90^{\circ}+66.27^{\circ}-87.48^{\circ}=\underline{\underline{68.79^{\circ}}}
$$

SPICE Results:

```
Problem H3P5-Open Loop Response of an LPLL with Lead-Lag Filter
VS 1 0 AC 1.0
R1 1 0 10K
* Loop bandwidth = Kv =2xE+6 Tau1=1E-4 Tau2=1E-5
ELPLL 2 0 LAPLACE {V(1)}=
+{(2E+6/(S+0.001))*((1+1E-5*S)/(1+1E-4*S))}
* Note: The 0.001 added to "S" in the denominator is to prevent
* blowup of the problem at low frequencies.
R2 2 0 10K
*Steady state AC analysis
.AC DEC 20 10 100K
.PRINT AC VDB(2) VP(2)
. PROBE
.END
```



The simulation results agree well with the calculated results.

